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SAIRAM

COLLEGE OF ENGINEERING

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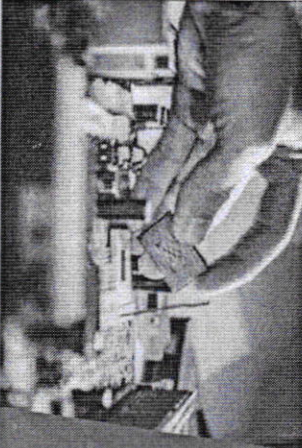


Department of



ELECTRONICS & COMMUNICATION ENGG.

Organizes



2 Days Workshop on "VLSI Design"

on 12th & 13th December 2022

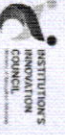
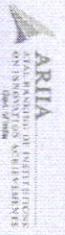
In Association with ISTE,
Bengaluru Chapter



Speaker

Prof. Sharan Basappa

Asst. Prof. DBIT, Bengaluru



Dr. Rathna Prof. P. Venugopal ISTE coordinator

Dr. T N Prabhakar Prof. & HOD, ECE

Dr. B. Shadaksharappa Principal

Dr. Sri Sai Prakash LeoMuthu Chairman & CEO, Sairam Institutions

Vision of the Institute

To emerge as a "Centre for excellence" offering Technical Education and Research opportunities of very high standards to students, develop the total personality of the individual, and instill high levels of discipline and strive to set global standards, making our students technologically superior and ethically strong, who in turn shall contribute to the advancement of society and humankind.

Vision of Dep.

Producing competent engineers with innovative ideas to meet the global needs and standards

PEOs of Dep.

- PEO1: To develop analytical technological skills with a professional attitude in the field of Electronics and communication Engineering.
- PEO2: To reform self-learning with technical potency in industry- oriented training.
- PEO3: To renew industrial technologies with professional estimation through attained knowledge in their chosen field.
- PEO4: To inculcate professional effectiveness with self-oriented industrial ethical revolution.

Mission of the Institute

We Dedicate and commit ourselves to achieve, sustain and foster unmatched excellence in Technical Education. To this end, we will pursue continuous development of infrastructure and enhance state-of-the-art equipment to provide our students a technological, up-to-date and intellectually inspiring environment of learning, research, creativity, innovation and professional activity and inculcate in them ethical and moral values.

Mission of Dep.

- M1. To produce graduates with technical expertise, professional attitude & ethical values
- M2. To reform the potency of Engineering through exploration-based learning
- M3. To create a passion amongst students for contributing to research by providing industry-oriented training.
- M4. To inculcate in the graduates, the thirst for self-learning & guide them to obtain knowledge in their chosen field.



15th December 2022

Report on - 2 Days Workshop under ISTE Student Chapter

Department of Electronics & Communication Engineering in association with ISTE Student chapter of our Institution organized 2 Days Workshop on VLSI Design for final year students on 12th & 13th of December 2022.

The programme started with Inaugural function held at Sir. M. Visvesvaraya AV hall on 12th December 2022. Prof. P. Venugopal, ISTE Coordinator of the department welcomed the head of the Department Dr. T.N Prabakar, Speaker of the workshop Dr. Sharanabasappa, Associate Professor, Don Bosco Institute of Technology, Bengaluru, College ISTE Coordinator Dr. Rathna, Department Staff and Final Year students of ECE.

Dr. T. N Prabakar, Head of ECE Department addressed the students, importance of Core Domains and their opportunities for the ECE Students. Dr. Rathna, College ISTE Coordinator expressed the benefits of ISTE Student membership for students carrier in her speech.

Session started with introduction to VLSI and career opportunities in VLSI for Electronics and Communication Engineering students. The main objective of this workshop is to learn the Full Custom IC Design Flow along with the usage of tools such as the Virtuoso Schematic Editor, Spectre, Virtuoso Layout Editor and Assura.

In this process, students created components like an Inverter, a NAND Gate, Common Source Amplifier. Students started the lab by creating a Library and attach it to a Technology Node gpdk180. By attaching it to a Technology Node, ensure that students go through the entire Front-end and Back-end process.

Students also created a new cell with the Schematic View, build the Schematic by instantiating various components, create a Symbol, build a test Schematic by instantiating the Symbol and verify the circuit using Spectre. Programme ended with Valedictory on 13th December 2022.

This 2 days' workshop built confidence in the students mind and helped the Students fraternity to enhance their skills in the field of VLSI Design.



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25 years of academic excellence

