



**Dr. T. N. Prabakar**

Professor

Department of Electronics & Communication Engineering

<p><b>Qualification</b></p>	<p><b>Ph. D - VLSI Design of Asynchronous Systems</b> (National Institute of Technology, Tiruchirappalli 620015) 2011</p> <p><b>M. Tech - Process Control &amp; Instrumentation</b> (Regional Engineering College, Tiruchirappalli 620015) 2002</p> <p><b>B. E - Instrumentation &amp; Control Engineering</b> ( A. K. College of Engineering, Krishnankoil) 1994</p>
<p><b>Experience</b></p>	<p>23 Years Academic &amp; 1 Year Research</p>
<p><b>Memberships</b></p>	<p>IEEE, ISOI</p>
<p><b>Area of Interest</b></p>	<p>VLSI, Embedded Systems, IoT</p>
<p><b>Publications</b></p>	<p><i>Patent</i>: 1 (First examination Report)</p> <p><i>International Journals</i>: 10</p> <p><i>International IEEE conferences</i>: 15</p> <p><i>Ph. D Guidance</i> : 01</p> <p>(Power aware Wireless Sensor Networks - Completed)</p>
<p><b>Research</b></p>	<p>Reviewer for IEEE, Taylor &amp; Francis conferences and journals in the Field of VLSI design.</p> <p>Ph. D work “INTASYCON” was fabricated as an ASIC Chip under the India Chip Programme 2010 sponsored by DIT, Government of India.</p> <p>Co-PI in DST – UKIERI Project on Design of Application specific Network on Chip with reliable custom topology for Aerospace and Internet of Things (2014-16)</p>