Academic Calendar of VTU, Belagavi for ODD Semester of 2020-21

First Semester - M. Tech/M. Arch/ MBA/MCA, I Semester (revised) B.E./B.Tech./B.Plan./B.Arch., and III Semester (revised) MCA

	I Semester M. Tech.	I Semester M. Arch.	I Semester MBA	I Semester MCA	I Semester B.E./B.Tech.,/ B.Plan.B.Arch	III Semester MCA
Commencement of ODD Semester	18.01.2021	18.01.2021	18.01.2021	18.01.2021	14.12.2020	01.09.2020
Last Working day of ODD Semester	17.04.2021	17.04.2021	17.04.2021	17.04.2021	31.03.2021	30.01.2021
Practical Examinations	03.05.2021 To 07.05.2021	--	--	03.05.2021 To 07.05.2021	05.04.2021 To 16.04.2021	04.02.2021 To 09.02.2021
Theory Examinations	19.04.2021 To 30.04.2021	19.04.2021 To 30.04.2021	19.04.2021 To 30.04.2021	19.04.2021 To 30.04.2021	19.04.2021 To 06.05.2021	11.02.2021 To 19.02.2021
Internship Viva-Voce	--	--	--	--	--	--
Professional training / Organization study	--	--	--	--	--	--
Commencement of EVEN Semester	10.05.2021	10.05.2021	10.05.2021	10.05.2021	10.05.2021	22.02.2021

Note:
- The Institute needs to function for **six days** a week with additional hours **(Saturday is a full working day).**
- The faculty/staff shall be available to undertake any work assigned by the university.
- If any of the above dates are declared to be a holiday then the corresponding event will come into effect on the next working day.
- Notification regarding the Calendar of Events relating to the conduct of University **Examinations** will be issued by the Registrar (Evaluation) from time to time.
- Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- In case if any changes are to be effected by Autonomous Colleges in the academic terms and examination schedule, they could do so with the approval of the University.

15-1-2021
REGISTRAR

Visvesvaraya Technological University

"Jnana Sangama" Belagavi-590018, Karnataka State, India

Dr. A. S. Deshpande B.E., M.Tech., Ph.D.
Registrar

Ref: VTU/BGM/SO2/2020-21/ 5296

Phone: (0831) 2498100
Fax: (0831) 2405467

Dated: **1 5 JAN 2021**

NOTIFICATION

Subject: Academic Calendar for I sem PG programmes and I sem (revised) B.E./B.Tech./B.Arch./B.Plan, III sem (revised) MCA for the year 2020-21 regarding...

Reference: Hon'ble Vice-Chancellor Approval dated 13.01.2021

Academic Calendar for I semester of M.Tech./M.Arch./MBA/MCA programmes, I sem (revised) B.E./B.Tech./B.Arch./B.Plan., and III sem (revised) MCA for the Year 2020-21 is hereby notified as enclosed.

The Principals of Affiliated, Constituent, and Autonomous Engineering Colleges are hereby informed to bring the contents of this Notification to the notice of all the concerned.

Sd/-

REGISTRAR

Encl: As mentioned above.

To,
1. The Principals of all affiliated/ constituent /Autonomous Engineering Colleges under the ambit of VTU Belagavi.
2. The Chairpersons of all Departments, Centres for PG Studies in Belagavi, Kalaburgi, Muddenahalli, and Mysore.

Copy to.
1. To the Hon'ble Vice-Chancellor through the secretary to VC, VTU Belagavi for information
2. The Registrar (Evaluation), VTU Belagavi for information.
3. The Regional Directors (I/c) of all the regional offices of VTU for circulation.
4. The Special Officer CNC VTU Belagavi for uploading on VTU website
5. PS to Registrar VTU Belagavi
6. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi
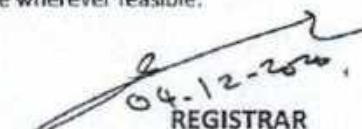
15-1-2021

REGISTRAR

Revised Academic Calendar of VTU, Belagavi for ODD Semester of 2020-21 (Tentative)

	I Sem B. E. / B. Tech. / B. Arch./B.Plan	I sem M.Tech./MBA /MCA/M.Arch.	III, V B. E. /B. Tech./B.Plan/ B.Arch & VII sem BPlan /BArch & IX Sem B. Arch.	VII Sem B. E. /B. Tech	III & V Sem MCA	III Sem MBA	III Sem M. Tech.	III Sem M. Arch.
Commencement of ODD Semester	14.12.2020		01.09.2020	01.09.2020	01.09.2020	01.09.2020	01.09.2020	01.09.2020
Last Working day of ODD Semester	25.03.2021		16.01.2021	16.01.2021	16.01.2021	16.01.2021	16.01.2021	16.01.2021
Practical Examinations	29.03.2021 Onwards#		21.01.2021 Onwards#	21.01.2021 Onwards#	08.02.2021 Onwards#	--	21.01.2021 Onwards#	--
Theory Examinations	12.04.2021 To 30.04.2021	Will be announced later	08.02.2021 To 27.03.2021	08.02.2021 To 27.03.2021	21.01.2021 To 06.02.2021	21.01.2021 To 19.02.2021	28.01.2021 To 13.02.2021	21.01.2021 To 06.02.2021
Internship			---	29.03.2021 To 10.04.2021	---	---	---	---
Internship Viva-Voce			---	---	---	---	15.02.2021 To 22.02.2021	---
Professional training / Organization study			---	---	---	22.02.2021 To 03.04.2021	---	---
Commencement of EVEN Semester	03.05.2021		29.03.2021	12.04.2021	15.02.2021	05.04.2021	23.02.2021	08.02.2021

NOTE:
- VII Semester B. E. / B. Tech. students shall have to undergo **Internship** as per circular of University **VTU/Aca/2019-20/85, dated 12.05.2020.**
- I Semester B. E/ B. Tech / B. Arch Students shall compulsorily undergo **Induction Program** for 01 Weeks.
- The classroom sessions for all the semesters would be in **ONLINE mode/blended** mode until further orders.
- The Institute needs to function for **six days** a week with additional hours (**Saturday is a full working day**).
- The faculty/staff shall be available to undertake any work assigned by the university.
- If any of the above dates are declared to be a holiday then the corresponding event will come into effect on the next working day.
- (#) Notification regarding the Calendar of Events relating to the conduct of **University Examinations** will be issued by the Registrar (Evaluation) from time to time.
- Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- Revised Academic Calendar is also applicable for **Autonomous Colleges.**
- The MBA students are permitted to carry out **project work** in blended mode (ONLINE/OFFLINE). More emphasis on OFFLINE mode wherever feasible.

04.12.20

REGISTRAR

Revised Academic Calendar of VTU, Belagavi for ODD Semester of 2020-21 (Tentative)

	I Sem B. E. / B. Tech. / B. Arch./B.Plan	I sem M.Tech./MBA /MCA/M.Arch.	III, V & VII Sem B. E. /B. Tech./B.Plan/ B.Arch & IX Sem B. Arch.	III & V Sem MCA	III Sem MBA	III Sem M. Tech.	III Sem M. Arch.
Commencement of ODD Semester			01.09.2020	01.09.2020	01.09.2020	01.09.2020	01.09.2020
Last Working day of ODD Semester			16.01.2021	16.01.2021	16.01.2021	16.01.2021	16.01.2021
Practical Examinations	Will be announced later	Will be announced later	21.01.2021 To 02.02.2021	21.01.2021 To 27.01.2021	--	21.01.2021 To 27.01.2021	--
Theory Examinations			08.02.2021 To 25.03.2021	28.01.2021 To 10.02.2021	21.01.2021 To 17.02.2021	28.01.2021 To 10.02.2021	21.01.2021 To 06.02.2021
Internship Viva-Voce			--	--	--	11.02.2021 To 18.02.2021	--
Professional training / Organization study			--	--	--	--	--
Commencement of EVEN Semester			26.03.2021	11.02.2021	18.02.2021	19.02.2021	08.02.2021

NOTE

- VII Semester B. E / B. Tech students shall have to undergo internship as per circular of University VTU/Aca/2019-20/85, dated 12.05.2020.
- The classroom sessions for all the higher semesters would be in ONLINE/OFFLINE/BLENDED as per the order issued by UGC/Govt. of Karnataka until further orders.
- The Institute needs to function for six days a week with additional hours (Saturday is a full working day)
- The faculty/staff shall be available to undertake any work assigned by the university.
- If any of the above dates are declared to be a holiday then the corresponding event will come into effect on the next working day.
- Notification regarding Calendar of Events relating to the conduct of University Examinations will be issued by the Registrar (Evaluation) from time to time.
- Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- Revised Academic Calendar is also applicable for Autonomous Colleges.

REGISTRAR

Visvesvaraya Technological University

"Jnana Sangama" Belagavi-590018, Karnataka State, India

Dr. A. S. Deshpande B.E., M.Tech., Ph.D.

Registrar

Ref: VTU/BGM/Aca/A-9/2020-21/ 3893

Phone: (0831) 2498100
Fax: (0831) 2405467

Dated: **25 NOV 2020**

NOTIFICATION

Subject: Revised Academic Calendar for ODD semester 2020-21(Tentative)regarding...

Reference: Hon'ble Vice-Chancellor Approval dated 17.11.2020

Revised Academic Calendar for III, V, and VII semesters of B.E./B.Tech./B.Plan and IX semester of B.Arch., III and V semester of MCA and III semesters of MBA, M.Tech., M.Arch., for the Academic Year 2020-21 is hereby notified as enclosed. The Principals of Affiliated, Constituent, and Autonomous Engineering Colleges are hereby informed to bring the contents of this Notification to the notice of all the concerned.

Sd/-

REGISTRAR

Encl: Revised Academic Calendar for odd semester of 2020-21(Tentative)

To,
1. The Principals of all affiliated/ constituent /Autonomous Engineering Colleges under the ambit of VTU Belagavi.
2. The Chairpersons of all Departments, Centres for PG Studies in Belagavi, Kalaburgi, Muddenahalli, and Mysore,

Copy to.
1. To the Hon'ble Vice-Chancellor through the secretary to VC, VTU Belagavi for information
2. The Registrar (Evaluation), VTU Belagavi for information.
3. The Regional Directors (I/c) of all the regional offices of VTU for circulation.
4. The Special Officer CNC VTU Belagavi for uploading on VTU website
5. PS to Registrar VTU Belagavi
6. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi

REGISTRAR

All Head's.

Tentative Academic Calendar of VTU, Belagavi for ODD Semester of 2020-2021

	I Sem B. E. / B. Tech. / B. Arch./B.Plan	I sem M.Tech./MBA /MCA/M.Arch.	III, V & VII Sem B. E. /B. Tech./B.Plan/ B.Arch & IX Sem B. Arch.	III & V Sem MCA	III Sem MBA	III Sem M. Tech.	III Sem M. Arch.
Commencement of ODD Semester	Will be announced later	Will be announced later	01.09.2020	01.09.2020	01.09.2020	01.09.2020	01.09.2020
Last Working day of ODD Semester			17.12.2020	17.12.2020	17.12.2020	17.12.2020	17.12.2020
Practical Examinations			21.12.2020 To 31.12.2020	21.12.2020 To 31.12.2020	21.12.2020 To 31.12.2020	21.12.2020 To 31.12.2020	21.12.2020 To 31.12.2020
Theory Examinations			04.01.2021 To 23.01.2021	04.01.2021 To 23.01.2021	04.01.2021 To 23.01.2021	04.01.2021 To 23.01.2021	04.01.2021 To 23.01.2021
Internship Viva-Voce			-	-	-	25.01.2021 To 08.02.2021	-
Professional training / Organization study			-	-	-		-
Commencement of EVEN Semester			08.02.2021	08.02.2021	08.02.2021	22.02.2021	08.02.2021

NOTE

- VII Semester B. E / B. Tech students shall have to undergo INTERNSHIP as per circular of University **VTU/Aca/2019-20/85, dated 12.05.2020.**
- I Semester B. E/ B. Tech / B. Arch Students shall compulsorily undergo Induction Program for a period of 3 Weeks as per the schedule given by VTU Belagavi
- The classroom sessions for all the higher semesters would be commencing from 01.09.2020(Tentative) in ONLINE mode until further orders.
- The Institute needs to function for six days a week with additional hours.
- The faculty/staff shall be available to undertake any work assigned by the university.
- If any of the above date is declared to be a holiday then the corresponding event will come into effect on the next working day.
- Notification regarding Calendar of Events relating to the conduct of University Examinations will be issued by the Registrar (Evaluation) from time to time.
- **Academic Calendar may be modified based on guidelines/directions issued in future by MHRD/UGC/AICTE/State Government.**

13/9/2020
Head of the Department
Electrical & Electronics Engineering
Sri Sairam College of Engineering

12.9.2020
REGISTRAR

Phone: (0831) 2498100
Fax: (0831) 2405467

Registrar

Ref: VTU/BGM/BOS/A9/2020-21 /6652   Date: 1 2 MAR 2021

Revised -NOTIFICATION

Subject: Academic Calendar for I semester (revised) B.E./B.Tech./B.Plan./B.Arch., for the year 2020-21 regarding...

Reference:
1. VTU/BGM/SO2/2020-21/5296, dated 15.01.2021
2. Hon'ble Vice-Chancellor's approval dated 12.03.2021

Revised Academic Calendar for I semester of B.E./B.Tech./B.Arch./B.Plan., for the Year 2020-21 is hereby notified as below-

Events	Dates
Commencement of ODD Semester	14.12.2020
Last Working day of ODD Semester	10.04.2021
Practical Examinations	05.05.2021 to 15.05.2021
Theory Examinations	19.04.2021 to 03.05.2021
Internship	-----
Internship Viva-Voce	-----
Professional training / Organization study	-----
Commencement of EVEN Semester	19.05.2021

The Principals of Affiliated, Constituent, and Autonomous Engineering Colleges are hereby informed to bring the contents of this Notification to the notice of all the concerned.

Sd/-

REGISTRAR

To,
1. The Principals of all affiliated/ constituent /Autonomous Engineering Colleges under the ambit of VTU Belagavi.
2. The Chairpersons of all Departments, Centres for PG Studies in Belagavi, Kalaburgi, Muddenahalli, and Mysore.

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4. The Special Officer CNC VTU Belagavi for uploading on VTU website
5. PS to Registrar VTU Belagavi
6. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi

REGISTRAR 12/03/21

1/1

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ

Visvesvaraya Technological University

Dr. A. S. Deshpande B.E., M.Tech., Ph.D.
Registrar

Phone: (0831) 2498100
Fax: (0831) 2405467

Ref: VTU/BGM/BOS/A9/2020-21 /948

Date: **15 APR 2021**

CIRCULAR

Subject: Commencement of EVEN semesters of UG programs for the year 2020-21 regarding...

Reference: Hon'ble Vice-Chancellor Approval dated 15.04.2021

Concerning the subject cited above, the commencement of EVEN semesters of B.E./B.Tech./B.Plan./B.Arch. programs of University will be from **19th April 2021**. The academic calendar related to the EVEN semester/s is notified as attached.

The Principals of Affiliated, Constituent, and Autonomous Engineering Colleges are hereby informed to bring the contents of this circular to the notice of all the concerned.

Sd/-

REGISTRAR

Encl: As mentioned above.
To,
1. The Principals of all affiliated/ constituent /Autonomous Engineering Colleges under the ambit of VTU Belagavi.
2. The Chairpersons of all Departments, Centres for PG Studies in Belagavi, Kalaburgi, Muddenahalli, and Mysore.

Copy to.
1. To the Hon'ble Vice-Chancellor through the secretary to VC, VTU Belagavi for information
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6. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi

REGISTRAR

Academic Calendar of EVEN semesters of UG Programmes for 2020-2021

Semesters / EVENTS	IV semester B.E./B.Tech.	IV semester B.Arch./ B.Plan.	VI semester B.E./B.Tech.	VI semester B.Plan./B.Arch	VIII semester B.E./B.Tech.	VII semester B.Plan./B.Arch.
Commencement of EVEN Semester	19.04.2021	19.04.2021	19.04.2021	19.04.2021	19.04.2021	19.04.2021
Last Working day of EVEN Semester	07.08.2021	07.08.2021	07.08.2021	07.08.2021	20.07.2021	20.07.2021
Practical Examinations	09.08.2021 To 19.08.2021	09.08.2021 To 19.08.2021	09.08.2021 To 19.08.2021	---	---	---
Theory Examinations	23.08.2021 To 09.09.2021	23.08.2021 To 09.09.2021	23.08.2021 To 09.09.2021	10.08.2021 To 31.08.2021	#22.07.2021 To 30.07.2021	#22.07.2021 To 30.07.2021
Internship	---	---	---	---	---	---
Internship Viva-Voce	---	---	---	---	02.08.2021 To 06.08.2021	---
Professional training / Organization study	---	---	---	---	---	---
Commencement of ODD Semester	13.09.2021	13.09.2021	13.09.2021	13.09.2021	---	09.08.2021 (IX sem Arch)

- The classroom sessions for even the semester should commence from the dates mentioned above. The classroom sessions for all the semesters would be in Offline /Online/blended mode until further orders.
- The Institute needs to function for six days a week with additional hours (Saturday is a full working day). #if required the college can plan to have extra classes even on Sundays also.
- If any of the above dates are declared to be a holiday then the corresponding event will come into effect on the next working day.
- Notification regarding the Calendar of Events relating to the conduct of University Examinations will be issued by the Registrar (Evaluation) from time to time.
- The faculty/staff shall be available to undertake any work assigned by the University.
- Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- Revised Academic Calendar is also applicable for Autonomous Colleges. In case if any changes are to be affected by Autonomous Colleges in the academic terms and examination schedule, they could do so with the approval of the University.

REGISTRAR

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ

"ವಿಜಯ ಅಧಿನಿಯಮ ೧೯೯೪ರ ಅಡಿಯಲ್ಲಿ, ಕರ್ನಾಟಕ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯ ವಿಶ್ವವಿದ್ಯಾಲಯ"
"ಜ್ಞಾನ ಸಂಗಮ", ಬೆಳಗಾವಿ–೫೯೦೦೧೮, ಕರ್ನಾಟಕ, ಭಾರತ

Visvesvaraya Technological University
(State University of Government of Karnataka Established as per the VTU Act, 1994)
"Jnana Sangama" Belagavi-590018, Karnataka, India
Phone: (0831) 2498100, Fax: (0831) 2405467, Website: vtu.ac.in

Dr. A. S. Deshpande B.E., M.Tech., Ph.D.
Registrar

Phone: (0831) 2498100
Fax: (0831) 2405467

Ref: VTU/BGM/BOS/A9/2020-21 / 345

Date: **2 1 APR 2021**

Revised - CIRCULAR

Subject: Commencement of EVEN semesters of UG programs for the year 2020-21 regarding...

Reference: Hon'ble Vice-Chancellor Approval dated 21.04.2021

Concerning the subject cited above, the revised - academic calendar related to the EVEN semester/s of B.E./B.Tech./B.Plan./B.Arch. programmes is notified as attached.

The Principals of Affiliated, Constituent, and Autonomous Engineering Colleges are hereby informed to bring the contents of this circular to the notice of all the concerned.

Sd/-

REGISTRAR

Encl: As mentioned above.

To,
1. The Principals of all affiliated/ constituent /Autonomous Engineering Colleges under the ambit of VTU Belagavi.
2. The Chairpersons of all Departments, Centres for PG Studies in Belagavi, Kalaburgi, Muddenahalli, and Mysore.

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3. The Regional Directors (I/c) of all the regional offices of VTU for circulation.
4. The Special Officer CNC VTU Belagavi for uploading on VTU website
5. PS to Registrar VTU Belagavi
6. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi

REGISTRAR

Revised-Academic Calendar of EVEN semesters of UG Programmes for 2020-2021

EVENTS	IV semester B.E./B.Tech.	IV semester B.Arch./ B.Plan.	VI semester B.E./B.Tech.	VI semester B.Plan./B.Arch	VIII semester B.E./B.Tech.	VIII semester B.Plan.	VIII semester B.Arch
Commencement of EVEN Semester	19.04.2021	19.04.2021	19.04.2021	19.04.2021	19.04.2021	19.04.2021	19.04.2021
Last Working day of EVEN Semester	07.08.2021	07.08.2021	07.08.2021	07.08.2021	#20.07.2021	#20.07.2021	07.08.2021
Practical Examinations	09.08.2021 To 19.08.2021	09.08.2021 To 19.08.2021	09.08.2021 To 19.08.2021	---	---	---	---
Theory Examinations	23.08.2021 To 09.09.2021	23.08.2021 To 09.09.2021	23.08.2021 To 09.09.2021	10.08.2021 To 31.08.2021	22.07.2021 To 30.07.2021	22.07.2021 To 30.07.2021	10.08.2021 To 17.08.2021
Internship	---	---	---	---	---	---	---
Internship Viva-Voce/ Project Viva-Voce	---	---	---	---	02.06.2021 To 06.06.2021	---	---
Professional training / Organization study	---	---	---	---	---	---	---
Commencement of ODD Semester	13.09.2021	13.09.2021	13.09.2021	13.09.2021	---	---	23.08.2021

- The classroom sessions for even the semester should commence from the dates mentioned above.
- The Institute needs to function for six days a week with additional hours (Saturday is a full working day). #if required the college can plan to have extra classes even on Sundays also.
- If any of the above dates are declared to be a holiday then the corresponding event will come into effect on the next working day.
- Notification regarding the Calendar of Events relating to the conduct of University Examinations will be issued by the Registrar (Evaluation) from time to time.
- The faculty/staff shall be available to undertake any work assigned by the university. Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- Revised Academic Calendar is also applicable for Autonomous Colleges. In case if any changes are to be affected by Autonomous Colleges, they could do so with the approval of the University.
- in the academic terms and examination schedule.

REGISTRAR

SRI SAIRAM COLLEGE OF ENGINEERING, ANEKAL, BENGALURU

ACADEMIC CALENDAR FOR THE EVEN (II, IV, VI, VIII) SEMESTER - 2020-2021

Day	April '21	Day	May ' 21	Day	June ' 21	Day	July'21	Day	August '21
T	1	S	1 May Day (H) - 1st Saturday (H)	T	1	T	1	S	1 Sunday (H)
F	2 Good Friday (H)	S	2 Sunday (H)	W	2	F	2	M	2 Internship Viva Voce Starts - 8 Sem
S	3 1st Saturday (H)	M	3	T	3	S	3 1st Saturday (H)	T	3
S	4 Sunday (H)	T	4	F	4 Monthly Attendance Submission	S	4 Sunday (H)	W	4
M	5	W	5 Monthly Attendance Submission	S	5 1st Saturday (H)	M	5 Monthly Attendance Submission	T	5 Final Attendance Submission
T	6 WGC - World Health Day	T	6	S	6 Sunday (H)	T	6	F	6 Internship Viva Voce Ends - 8 Sem
W	7	F	7	M	7	W	7 Project Review - Final	S	7 VTU Last Working day - 1st Saturday (H)
T	8	S	8 ISTE Activity (EEE)	T	8	T	8	S	8 Sunday (H)
F	9	S	9 Sunday (H)	W	9 Project Review - 3	F	9	M	9 Practical Exam Starts - 2,4,6 Sem
S	10	M	10	T	10	S	10 ISTE Activity (ECE)	T	10
S	11	T	11	F	11	S	11 Sunday (H)	W	11
M	12	W	12 Project Review - 1	S	12 ISTE Activity (MECH)	M	12 IA Test -3 - 8 Sem	T	12
T	13 Chandraman Ugadi (H)	T	13	S	13 Sunday (H)	T	13 IA Test -3 - 8 Sem	F	13
W	14 Dr.B.R.Ambedkar Jayanthi (H)	F	14 Basava Jayanthi & RAMAZAN (H)	M	14	W	14 IA Test -3 - 8 Sem	S	14
T	15	S	15 3rd Saturday (H)	T	15	T	15	S	15 Sunday (H)
F	16	S	16 Sunday (H)	W	16 ISTE Activity (MECH)	F	16 Project Exhibition - 2021	M	16
S	17 3rd Saturday (H)	M	17	T	17	S	17 3rd Saturday (H)	T	17
S	18 Sunday (H)	T	18	F	18	S	18 Sunday (H)	W	18
M	19 Commencement of Even Semester	W	19	S	19 3rd Saturday (H)	M	19 Lab Internals Starts -2,4,6 Sem	T	19 Practical Exam Ends - 2,4,6 Sem
T	20	T	20	S	20 Sunday (H)	T	20 8 Sem - Last Working Day	F	20 Last day of Moharam (H)
W	21	F	21	M	21	W	21 Bakrid (H)	S	21 3rd Saturday (H)
T	22	S	22 ISTE Activity (ECE)	T	22	T	22 VTU Theory Exam Starts - 8 Sem	S	22 Sunday (H)
F	23	S	23 Sunday (H)	W	23 Project Review - 4	F	23 Lab Internals Ends -2,4,6 Sem	M	23 VTU Theory Exam Starts - 2,4,6 Sem
S	24 ISTE Activity (CSE)	M	24 IA Test -1	T	24	S	24 ISTE Activity (CSE)	T	24 Stock Verification Starts
S	25 Sunday (H)	T	25 IA Test -1	F	25	S	25 Sunday (H)	W	25
M	26	W	26 IA Test -1	S	26 ISTE Activity (EEE)	M	26 IA Test -3 - 2,4,6 Sem	T	26 International Conference - 2021
T	27	T	27 Project Review - 2	S	27 Sunday (H)	T	27 IA Test -3 - 2,4,6 Sem	F	27
W	28	F	28	M	28 IA Test -2	W	28 IA Test -3 - 2,4,6 Sem	S	28
T	29	S	29 5th Saturday (H)	T	29 IA Test -2	T	29	S	29 Sunday (H)
F	30	S	30 Sunday (H)	W	30 IA Test -2	F	30 VTU Theory Exam Ends - 8 Sem	M	30
		M	31			S	31 5th Saturday (H)	T	31 Stock Verification Ends

❖ Note: Industrial Visits may be arranged before the month of June 2021.

No. of Monday's : 16   No. of Tuesday's : 16   No. of Wednesday's : 15   No. of Thursday's : 16   No. of Friday's : 15   No. of Saturday's : 07

Total No. of Working days: 85 for 2,4,6 Sem & 72 for 8th Sem

Commencement of ODD Semester: 13.09.2021

Principal   12/04/2021

SRI SAIRAM COLLEGE OF ENGINEERING, ANEKAL, BENGALURU

ACADEMIC CALENDAR FOR THE ODD I st SEMESTER - 2020-2021

Day	December '20	Day	January '21	Day	February '21	Day	March '21	Day	April '21	Day	May '21
T	1	F	1	M	1	M	1	T	1 IA Test -3	S	1 May Day (H) - 1st Saturday (H)
W	2	S	2 1st Saturday (H)	T	2	T	2	F	2 Good Friday (H)	S	2 Sunday (H)
T	3 Kanakadasa Jayanthi (H)	S	3 Sunday (H)	W	3	W	3	S	3 1st Saturday (H)	M	3
F	4	M	4	T	4	T	4	S	4 Sunday (H)	T	4
S	5 1st Saturday (H)	T	5	F	5	F	5	M	5 VTU Practical Exam Starts	W	5
S	6 Sunday (H)	W	6	S	6 1st Saturday (H)	S	6 1st Saturday (H)	T	6	T	6 VTU Theory Exam Ends
M	7	T	7 UHV Orientation Program Starts	S	7 Sunday (H)	S	7 Sunday (H)	W	7	F	7
T	8	F	8	M	8	M	8 IA Test -2	T	8	S	8
W	9	S	9 Tuesday (TT)	T	9	T	9 IA Test -2	F	9	S	9 Sunday (H)
T	10	S	10 Sunday (H)	W	10	W	10 IA Test -2	S	10	M	10 Commencement of EVEN Semester
F	11	M	11	T	11	T	11 Mahashivaratri (H)	S	11 Sunday (H)	T	11
S	12	T	12	F	12	F	12	M	12 Submission of Progress Report	W	12 Eid al - Fiir (H)
S	13 Sunday (H)	W	13 UHV Orientation Program Ends	S	13 Friday (TT)	S	13 Thursday (TT)	T	13 Chandhramana Ugadhi (H)	T	13
M	14 Commencement of ODD Semester	T	14 Makara Sankranthi (H)	S	14 Sunday (H)	S	14 Sunday (H)	W	14 Dr.B.R.Ambedkar Jayanthi (H)	F	14
T	15	F	15	M	15 IA Test -1	M	15	T	15	S	15 3rd Saturday (H)
W	16	S	16 3rd Saturday (H)	T	16 IA Test -1	T	16 Balipadyami / Deepavali (H)	F	16 VTU Practical Exam Ends	S	16 Sunday (H)
T	17	S	17 Sunday (H	W	17 IA Test -1	W	17	S	17 3rd Saturday (H)	M	17
F	18	M	18 ICT Academy - Power Seminar	T	18	T	18	S	18 Sunday (H)	T	18
S	19 3rd Saturday (H)	T	19	F	19	F	19 Submission of Progress Report	M	19 VTU Theory Exam Starts	W	19
S	20 Sunday (H)	W	20	S	20 3rd Saturday (H)	S	20 3rd Saturday (H)	T	20	T	20
M	21	T	21	S	21 Sunday (H)	S	21 Sunday (H)	W	21	F	21
T	22	F	22 Submission of Progress Report	M	22	M	22 Lab Internal Starts	T	22	S	22
W	23	S	23 Thursday (TT)	T	23	T	23	F	23	S	23 Sunday (H)
T	24	S	24 Sunday (H)	W	24	W	24	S	24	M	24
F	25 X - Mas (H)	M	25	T	25	T	25	S	25 Sunday (H)	T	25
S	26 Thursday (TT)	T	26 Republic Day (H)	F	26 Submission of Progress Repor	F	26 Lab Internal Ends	M	26	W	26
S	27 Sunday (H)	W	27	S	27 Thursday (TT)	S	27 Tuesday (TT)	T	27	T	27
M	28	T	28	S	28 Sunday (H)	S	28 Sunday (H)	W	28	F	28
T	29	F	29			M	29	T	29	S	29
W	30	S	30 5th Saturday (H)			T	30 IA Test -3	F	30	S	30 Sunday (H)
T	31	S	31 Sunday (H)			W	31 Last Working Day - IA Test -3			M	31

❖ Note: Industrial Visits to be arranged during the month of November 2021.

No. of Monday's : 15   No. of Tuesday's : 14   No. of Wednesday's : 15   No. of Thursday's : 13   No. of Friday's : 14

Total No. of Working days: 71 + 7 = 78

Commencement of Even Semester: 1st Year - 10.05.2021

Principal 02/12/2020

SRI SAIRAM COLLEGE OF ENGINEERING, ANEKAL, BENGALURU

ACADEMIC CALENDAR FOR THE ODD (III, V, VII) SEMESTER - 2020-2021

August '20

Day	Date	Event
S	1	1st Saturday - Bakrid (H)
S	2	Sunday (H)
M	3	
T	4	
W	5	
T	6	
F	7	
S	8	
S	9	Sunday (H)
M	10	
T	11	
W	12	
T	13	
F	14	
S	15	3rd Saturday - Independence Day (H)
S	16	Sunday (H)
M	17	
T	18	
W	19	
T	20	Orientation Program - Higher Sem
F	21	
S	22	Vinayaka Chaturti (H)
S	23	Sunday (H)
M	24	Commencement of ODD Semester
T	25	
W	26	
T	27	
F	28	
S	29	5th Saturday (H)
S	30	Sunday (H)
M	31	

September ' 20

Day	Date	Event
T	1	
W	2	
T	3	
F	4	
S	5	Teacher's Day Celebration
S	6	Sunday (H)
M	7	
T	8	
W	9	
T	10	
F	11	.
S	12	
S	13	Sunday (H)
M	14	
T	15	Engineer's Day
W	16	
T	17	Mahalaya Amavasye (H)
F	18	
S	19	3rd Saturday (H)
S	20	Sunday (H)
M	21	
T	22	
W	23	
T	24	
F	25	
S	26	
S	27	Sunday (H)
M	28	
T	29	
W	30	7th Sem - Project Group Finalization

October ' 20

Day	Date	Event
T	1	
F	2	Mahatma Gandhi Jayanthi (H)
S	3	1st Saturday (H)
S	4	Sunday (H)
M	5	IA Test -1
T	6	IA Test -1
W	7	IA Test -1
T	8	
F	9	IEI Activity (CSE)
S	10	
S	11	Sunday (H)
M	12	
T	13	
W	14	
T	15	7th Sem - Project Synopsis submission
F	16	
S	17	3rd Saturday (H)
S	18	Sunday (H)
M	19	Submission of Progress Report
T	20	Poster Presentation Contest-IETE Activity(EEE)
W	21	Webinar on 5G - ISTE Activity (ECE)
T	22	
F	23	29th ISO Internal Audit
S	24	
S	25	Sunday - Ayuda Pooja (H)
M	26	Vijaya Dasami (H)
T	27	
W	28	IEI Activity (MECH)
T	29	Guest Lecture - CSI Activity
F	30	Eid - Milad (H)
S	31	Maharshi Valmiki Jayanti (H)

November '20

Day	Date	Event
S	1	Sunday - Kannada Rajyotsava (H)
M	2	
T	3	CSI / ISTE Activity (CSE)
W	4	
T	5	Webinar on ML & AI - IEI Activity (ECE)
F	6	
S	7	1st Saturday (H)
S	8	Sunday (H)
M	9	IA Test -2
T	10	IA Test -2
W	11	IA Test -2
T	12	Guest Lecture - CSI Activity
F	13	Internship Awareness - IETE/ISTE Activity (EEE)
S	14	Naraka Chaturdashi
S	15	Sunday (H)
M	16	Balipadyami / Deepavali (H)
T	17	
W	18	SAE / ISTE Activity (MECH)
T	19	Milagro'20- (EEE)
F	20	Guest Lecture - IETE Activity (ECE)
S	21	3rd Saturday (H)
S	22	Sunday (H)
M	23	Submission of Progress Report
T	24	Hands-On Training (NT-ECE)
W	25	Guest Lecture - IEI Activity (EEE)
T	26	
F	27	TQM Audit - 5 S
S	28	Workshop - (NT-EEE)
S	29	Sunday (H)
M	30	27th Management Review Meeting

December '20

Day	Date	Event
T	1	
W	2	
T	3	Kanakadasa Jayanthi (H)
F	4	
S	5	1st Saturday (H)
S	6	Sunday (H)
M	7	i7C - Conference - 2020
T	8	
W	9	MoU Activity - Dristi Contest - ECE
T	10	
F	11	
S	12	
S	13	Sunday (H)
M	14	
T	15	
W	16	
T	17	
F	18	
S	19	3rd Saturday (H)
S	20	Sunday (H)
M	21	Stock Verification Starts
T	22	IA Test -3
W	23	IA Test -3
T	24	IA Test -3
F	25	X - Mas (H)
S	26	
S	27	Sunday (H)
M	28	
T	29	Submission of Progress Report
W	30	
T	31	Stock Verification Ends

❖ Note: Industrial Visits to be arranged during the month of November 2020. (Theory Examinations of 1, 3, 5 & 7 Semester - Notified later)

No. of Monday's :15   No. of Tuesday's : 16   No. of Wednesday's : 16   No. of Thursday's : 16   No. of Friday's : 15

Total No. of Working days: 87

Commencement of ODD Semester: 1st Year - 01. 11. 2020 (Tentative)

10/08/2020

Principal

SAIRAM
COLLEGE OF ENGINEERING
Anekal, Bengaluru

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

CALENDAR OF EVENTS / ACADEMIC YEAR 2020-21 / EVEN SEMESTER

WEEK NO.	MONTH	DAYS						ACTIVITIES / EVENTS
		MON	TUE	WED	THU	FRI	SAT	
1	APRIL '21				1	2(H)	3(H)	2 – Good Friday 3 – 1st Saturday
2	APRIL	5	6	7	8	9	10	6 – World Health Day
3	APRIL	12	13(H)	14(H)	15	16	17(H)	13 – Chandraman Ugadi 14 – Dr.B.R.Ambedkar Jeyanthi 17 – 3rd Saturday
4	APRIL	19	20	21	22	23	24	19 – Commencement of Even Semester
5	APRIL/ MAY	26	27	28	29	30	1(H)	1 – May Day & 1st Saturday
6	MAY	3	4	5	6	7	8	5 – Monthly Attendance Submission
7	MAY	10	11	12	13	14(H)	15(H)	12 – Project reveiew-1 14 – Basava Jayanthi & Ramzan 15 – 3rd Saturday
8	MAY	17	18	19	20	21	22	
9	MAY	24	25	26	27	28	29(H)	24, 25 & 26 – IA Test-1 27 – Project reveiew-2 29 – 5th Saturday
10	MAY / JUNE	31	1	2	3	4	5(H)	4 – Monthly Attendance Submission 5 – 1st Saturday
11	JUNE	7	8	9	10	11	12	9 – Project Review-3
12	JUNE	14	15	16	17	18	19(H)	19– 3rd Saturday
13	JUNE	21	22	23	24	25	26	23– Project Review-4
14	JUNE / JULY	28	29	30	1	2	3(H)	28, 29 & 30 – IA Test-2 3 –1st Saturday
15	JULY	5	6	7	8	9	10	5 – Monthly Attendance Submission 7 – Project Review Final
16	JULY	12	13	14	15	16	17(H)	12, 13 & 14 – IA Test-3 – 8th Semester 16 – Project Exhibition 2021 17– 3rd Saturday
17	JULY	19	20	21(H)	22	23	24	19– Lab Internals Starts – 2nd, 4th & 6th Semester 20 – Last working Day – 8th Semester 22 – Bakrid 22 – VTU Theory Exam Starts – 8th Semester 23– Lab Internals Ends – 2nd, 4th & 6th Semester

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

18	JULY	26	27	28	29	30	31(H)	26, 27 & 28 – IA Test-3 – 2nd, 4th & 6th Semester 30 –VTU Theory Exam Ends – 8th Semester 31 – 5th Saturday
19	AUGUST	2	3	4	5	6	7(H)	2 – Internship Viva Voce Starts – 8th Semester 5 – Final Attendance Submission 6 – Internship Viva Voce Ends – 8th Semester 7 – VTU Last working Day & 1st Saturday (H)
20	AUGUST	9	10	11	12	13	14	9 –Practical Exam Starts – 2nd, 4th & 6th Semester
21	AUGUST	16	17	18	19	20(H)	21(H)	19 –Practical Exam Ends – 2nd, 4th & 6th Semester 20 – Last day of Moharam 21– 3rd Saturday
22	AUGUST	23	24	25	26	27	28	23 –VTU Theory Exam Starts – 2nd, 4th & 6th Semester 24 – Stock Verification Starts 26 – International Conference 2021
23	AUGUST'21	30	31					31 – Stock Verification Ends

NOTE:

❖ Attendance shortage list should be displayed once in a month.
❖ Staff Meeting with HOD should be conducted once in a month. If required, it should be conducted after completing every HOD's meeting by Principal.
❖ Seminar, Workshop, Training Programs, Remedial Classes, Tutorials, Special Classes etc., should be conducted.
❖ Number of hours allotted for each theory subjects is as per VTU norms.

Dept. Academic Coordinator   HOD/ECE   Principal

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DEPARTMENT OFELECTRONICS & COMMUNICATION ENGINEERING

CALENDAR OF EVENTS / ACADEMIC YEAR 2020-21/ ODD SEMESTER

WEEK NO.	MONTH	DAYS						ACTIVITIES / EVENTS
		MON	TUE	WED	THU	FRI	SAT	
01	AUG				20	21	22	20- Orientation Program for higher Semester.
02	AUG	24	25	26	27	28	29	24th August 2020 Commencement Of Odd Semester. 29-5th Saturday holiday
03	AUG/SEP	31	1	2	3	4	5	5-Teacher's day celebration
04	SEP	7	8	9	10	11	12	
05	SEP	14	15	16	17	18	19	15-Engineer's Day 17-Mahalaya Amavasya
06	SEP	21	22	23	24	25	26	
07	SEP/OCT	28	29	30	1	2	3	30-Project Group Finalization 3-1st Saturday 2- Gandhi Jayanthi
08	OCT	5	6	7	8	9	10	5,6,7-IA-1
09	OCT	12	13	14	15	16	17	15-Project Synopsis Submission 16-Webinar on 5G-ISTE Activity 17-3rd Saturday
10	OCT	19	20	21	22	23	24	19- Progress Report Submission 20-Poster Presentation contest-IETE Activity 23-29th ISO Internal Audit
11	OCT	26	27	28	29	30	31	25-Ayuda Pooja 26-Vijaya Dasami 28-IEI Activity -Mech 30-Ed Milad 31-valmihi jeyanthi 31-Valmigi Jayanthi
12	NOV	2	3	4	5	6	7	5-Webinar on AI & ML-IEI Activity 7-1st saturday
13	NOV	9	10	11	12	13	14	9-11-IA 2 13-ISTE/IETE Activity-Internship Awareness 14-Naraka Chaturdasi
14	NOV	16	17	18	19	20	21	16-Balipadyami/Deepavali 18-SAE.ISTE Activity 20-Guest Lecture-IEI Activity
15	NOV	23	24	25	26	27	28	23- Progress Report Submission 27-TOM Audit-5s
16	NOV/DEC	30	1	2	3	4	5	30-27th Management Review Meeting 3-Kanakadasa Jayanthi 5-1st saturday
17	DEC	7	8	9	10	11	12	9-MOU Activity-DristiContest 7-I7C Conference-2020
18	DEC	14	15	16	17	18	19	
19	DEC	21	22	23	24	25	26	21-Stock Verification starts 25-X Mas
20	DEC	28	29	30	31			29-Submission of Progress report

NOTE:

- Attendance shortage list should be displayed once in a month
- Staff Meeting with HOD should be conducted once in a month.
- Seminar, Workshop, Training Programs, Remedial Classes, Tutorials, Special Classes etc., should be conducted.
- Number of hours allotted for each theory subjects is as per VTU norms.

Dept.Academic Co ordinator   Head of the Dept.   Principal

VISVESVARAYA TECHNOLOGICAL UNIVERSITY BELAGAVI

3rd to 8th Semester BE- Electronics & Communication
Scheme of Teaching and Examinations
Outcome Based Education (OBE) and Choice Based Credit System (CBCS)
(Effective from the academic year 2018 – 19)

27/7/19

Head of the Department

Sri Sairam College of Engineering

PRINCIPAL
Sri Sairam College Of Engineering
Sai Leo Nagar, Guddanahalli Post,
Anekal, Bengaluru - 562 106

B.E: Electronics & Communication Engineering

Program Outcomes (POs)

At the end of the B.E program, students are expected to have developed the following outcomes.

1. **Engineering Knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialisation to the solution of complex engineering problems.

2. **Problem analysis:** Identify, formulate, research literature, and analyse complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.

3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.

4. **Conduct investigations of complex problems:**Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

5. **Modern Tool Usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.

6. **The Engineer and Society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal, and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

7. **Environment and Sustainability:**Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of need for sustainable development.

8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

9. **Individual and Team Work:**Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

10. **Communication:**Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

11. **Project Management and Finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

12. **Life-long learning:** Recognise the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change

Program Specific Outcomes (PSOs)

At the end of the B E Electronics & Communication Engineering program, students are expected to have developed the following program specific outcomes.

PSO1: Specify, design, build and test analog and digital systems for signal processing including multimedia applications, using suitable components or simulation tools.

PSO2: Understand and architect wired and wireless analog and digital communication systems as per specifications, and determine their performance.

Note

1. The Course Outcomes and RBT levels indicated for each course in the syllabus are indicative/suggestive. The faculty can set them appropriately according to their lesson plan.

2. The Question Paper format for the theory courses is as follows:

Question Paper Pattern for Theory Courses (2018 Scheme):

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks
- Each full question can have a maximum of 4 sub questions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2018 – 19
Outcome Based Education(OBE) and Choice Based Credit System (CBCS)
(Effective from the academic year 2018 – 19)

III SEMESTER

Sl. No	Course and Course Code		Course Title	Teaching Department	Theory Lecture (L)	Tutorial (T)	Practical/Drawing (P)	Duration in hours	CIE Marks	SER Marks	Total Marks	Credits
1	BSC	18MAT31	Transform Calculus, Fourier Series and Numerical Techniques	Mathematics	2	2	--	03	40	60	100	
2	PCC	18EC32	Network Theory		3	2	--	03	40	60	100	
3	PCC	18EC33	Electronic Devices		3	0	--	03	40	60	100	
4	PCC	18EC34	Digital System Design		3	0	--	03	40	60	100	
5	PCC	18EC35	Computer Organization & Architecture		3	0	--	03	40	60	100	
6	PCC	18EC36	Power Electronics & Instrumentation		3	0	--	03	40	60	100	
7	PCC	18ECL37	Electronic Devices & Instrumentation Laboratory		--	2	2	03	40	60	100	
8	PCC	18ECL38	Digital System DesignLaboratory		--	2	2	03	40	60	100	
9	HSMC	18KVK39/49	Vyavaharika Kannada (Kannada for communication)/	HSMC	–	2	--	--	100	--	100	
		18KAK39/49	Aadalitha Kannada (Kannada for Administration)									
			OR					03	40	60		
		18CPC39/49	Constitution of India, Professional Ethics and Cyber Law		1	–	–	Examination is by objective type questions				
			TOTAL		17 OR 18	10 OR 08	04	24 OR 27	420 OR 360	480 OR 540	900	24

Note: BSC: Basic Science, PCC: Professional Core, HSMC: Humanity and Social Science, NCMC: Non-credit mandatory course.

18KVK39Vyavaharika Kannada (Kannada for communication) is for non-kannada speaking, reading and writing students and 18KAK39 Aadalitha Kannada (Kannada for Administration) is for students who speak, read and write kannada.

Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs

10	NC MC	18MATDIP31	Additional Mathematics - I	Mathematics	02	01	--	03	40	60	100	0

(a)The mandatory non – credit courses Additional Mathematics I and II prescribed for III and IV semesters respectively, to the lateral entry Diploma holders admitted to III semester of BE/B.Tech programs,shall attend the classes during therespective semesters to complete all the formalities of the course and appear for the University examination.In case, any student fails to register for the said course/fails to secure the minimum 40 % of the prescribed CIE marks, he/she shall be deemed to have secured F grade. In such a case, the students have to fulfill the requirements during subsequent semester/s to appear for SEE.

(b)These Courses shall not beconsidered for vertical progression, but completion of the courses shall be mandatory for the award of degree.

Courses prescribed to lateral entry B. Sc degree holders admitted to III semester of Engineering programs

Lateral entrant students from B.Sc. Stream, shall clear the non-credit courses Engineering Graphics and Elements of Civil Engineering and Mechanics of the First Year Engineering Programme. These Courses shall not be considered for vertical progression, but completion of the courses shall be mandatory for the award of degree.

AICTE Activity Points to be earned by students admitted to BE/B.Tech/B.Plan day college programme (For more details refer to Chapter 6.AICTE Activity Point Programme, Model Internship Guidelines):

Over and above the academic grades, every Day College regular student admitted to the 4 years Degree programme and every student entering 4 years Degree programme through lateral entry, shall earn 100 and 75 Activity Points respectively for the award of degree through AICTE Activity Point Programme. Students transferred from other Universities to fifth semester are required to earn 50 Activity Points from the year of entry to VTU. The Activity Points earned shall be reflected on the student's eighth semester Grade Card.

The activities can be can be spread over the years, anytime during the semester weekends and holidays, as per the liking and convenience of the student from the year of entry to the programme. However, minimum hours' requirement should be fulfilled. Activity Points (non-credit) have no effect on SGPA/CGPA and shall not be considered for vertical progression.

In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2018 – 19
Outcome Based Education(OBE) and Choice Based Credit System (CBCS)
(Effective from the academic year 2018 – 19)

IVSEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Theory Lecture (L)	Tutorial (T)	Practical/ Drawing (P)	Duration in hours	CIE Marks	SEE Marks	Total Marks	Credits
1	BSC	18MAT41	Complex Analysis, Probability and Statistical Methods	Mathematics	2	2	--	03	40	60	100	3
2	PCC	18EC42	Analog Circuits		3	2	--	03	40	60	100	4
3	PCC	18EC43	Control Systems		3	0	--	03	40	60	100	3
4	PCC	18EC44	Engineering Statistics & Linear Algebra		3	0	--	03	40	60	100	3
5	PCC	18EC45	Signals & Systems		3	0	--	03	40	60	100	3
6	PCC	18EC46	Microcontroller		3	0	--	03	40	60	100	3
7	PCC	18ECL47	Microcontroller Laboratory		--	2	2	03	40	60	100	2
8	PCC	18ECL48	Analog Circuits Laboratory		--	2	2	03	40	60	100	2
9	HSMC	18KVK39/49	Vyavaharika Kannada (Kannada for communication)	HSMC	--	2	--	--	100	--	100	1
		18KAK39/49	Aadalitha Kannada (Kannada for Administration)									
		OR										
		18CPC39/49	Constitution of India, Professional Ethics and Cyber Law		1	--	--	03	40	60		
					Examination is by objective type questions							
			TOTAL		17 OR 18	10 OR 08	04	24 OR 27	420 OR 360	480 OR 540	900	24

Note: BSC: Basic Science, PCC: Professional Core, HSMC: Humanity and Social Science, NCMC: Non-credit mandatory course.
18KVK39/49 Vyavaharika Kannada (Kannada for communication) is for non-kannada speaking, reading and writing students and 18KAK39/49 Aadalitha Kannada (Kannada for Administration) is for students who speak, read and write kannada.

Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs
| 10 | NCMC | 18MATDIP41 | Additional Mathematics – II | Mathematics | 02 | 01 | -- | 03 | 40 | 60 | 100 | 0 |

((a)The mandatory non – credit courses Additional Mathematics I and II prescribed for III and IV semesters respectively, to the lateral entry Diploma holders admitted to III semester of BE/B.Tech programs, shall attend the classes during the respective semesters to complete all the formalities of the course and appear for the University examination. In case, any student fails to register for the said course/ fails to secure the minimum 40 % of the prescribed CIE marks, he/she shall be deemed to have secured F grade. In such a case, the student have to Fulfill the requirements during subsequent semester/s to appear for SEE.
(b) These Courses shall not be considered for vertical progression, but completion of the courses shall be mandatory for the award of degree.

Courses prescribed to lateral entry B. Sc degree holders admitted to III semester of Engineering programs
Lateral entrant students from B.Sc. Stream, shall clear the non-credit courses Engineering Graphics and Elements of Civil Engineering and Mechanics of the First Year Engineering Programme. These Courses shall not be considered for vertical progression, but completion of the courses shall be mandatory for the award of degree.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2018 – 19
Outcome Based Education(OBE) and Choice Based Credit System (CBCS)
(Effective from the academic year 2018 – 19)

V SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	HSMC	18ES51	Technological Innovation Management And Entrepreneurship		3	0	--	03	40	60	100	3
2	PCC	18EC52	Digital Signal Processing		3	2	--	03	40	60	100	4
3	PCC	18EC53	Principles of Communication Systems		3	2	--	03	40	60	100	4
4	PCC	18EC54	Information Theory & Coding		3	--	--	03	40	60	100	3
5	PCC	18EC55	Electromagnetic Waves		3	--	--	03	40	60	100	3
6	PCC	18EC56	Verilog HDL		3	--	--	03	40	60	100	3
7	PCC	18ECL57	Digital Signal Processing Laboratory		--	2	2	03	40	60	100	2
8	PCC	18ECL58	HDL Laboratory		--	2	2	03	40	60	100	2
9	HSMC	18CIV59	Environmental Studies	Civil/ Environmental [Paper setting: Civil Engineering Board]	1	--	--	02	40	60	100	1
				TOTAL	19	08	4	26	360	540	900	25

Note: PCC:Professional Core, HSMC: Humanity and Social Science.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2018 – 19
Outcome Based Education(OBE) and Choice Based Credit System (CBCS)
(Effective from the academic year 2018 – 19)

VI SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week Theory Lecture L	Tutorial T	Practical/ Drawing P	Duration in hours	CIE Marks	SEE Marks	Total Marks	Credits
1	PCC	18EC61	Digital Communication		3	2	--	03	40	60	100	4
2	PCC	18EC62	Embedded Systems		3	2	--	03	40	60	100	4
3	PCC	18EC63	Microwave & Antennas		3	2	--	03	40	60	100	4
4	PEC	18XX64X	Professional Elective - 1		3	--	--	03	40	60	100	3
5	OEC	18XX65X	Open Elective –A		3	--	--	03	40	60	100	3
6	PCC	18ECL66	Embedded Systems Laboratory		--	2	2	03	40	60	100	2
7	PCC	18ECL67	Communication Laboratory		--	2	2	03	40	60	100	2
8	MP	18ECMP68	Mini-project		--	--	2	03	40	60	100	2
9	Internship	--	Internship	To be carried out during the vacation/s of VI and VII semesters and /or VII and VIII semesters.								
			TOTAL		**15**	**10**	**6**	**24**	**320**	**480**	**800**	**24**

Note: PCC: Professional core, PEC: Professional Elective, OE: Open Elective, MP: Mini-project.

Professional Elective -1	
Course code under 18XX64X	Course Title
18EC641	Operating System
18EC642	Artificial Neural Networks
18EC643	Object Oriented Programming using C++
18EC644	Digital System Design using Verilog
18EC645	Nanoelectronics

Open Elective –A
(i) 18EC651 Signal Processing   (ii)18EC652 Sensors &Signal Conditioning

Students can select any one of the open electives offered by other Departments except those that are offered by the parent Department (Please refer to the list of open electives under 18XX65X).

Selection of an open elective shall not be allowed if,

* The candidate has studied the same course during the previous semesters of the programme.
* The syllabus content of open elective is similar to that of the Departmental core courses or professional electives.
* A similar course, under any category, is prescribed in the higher semesters of the programme.

Registration to electives shall be documented under the guidance of Programme Coordinator/ Advisor/Mentor.

Mini-project work:

Based on the ability/abilities of the student/s and recommendations of the mentor, a single discipline or a multidisciplinary Mini- project can be assigned to an individual student or to a group having not more than 4 students.

CIE procedure for Mini-project:

(i) Single discipline:The CIE marks shall be awarded by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the Guide.

The CIE marks awarded for the Mini-project work, shall be based on the evaluation of project report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates.

(ii) Interdisciplinary: Continuous Internal Evaluation shall be group wise at the college level with the participation of all the guides of the college.

The CIE marks awarded for the Mini-project, shall be based on the evaluation of project report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates.

SEE for Mini-project:

(i) Single discipline: Contribution to the Mini-project and the performance of each group member shall be assessed individually in the semester end examination (SEE) conducted at the department.

(ii) Interdisciplinary: Contribution to the Mini-project and the performance of each group member shall be assessed individually in semester end examination (SEE) conducted separately at the departments to which the student/s belong to.

Internship: All the students admitted to III year of BE/B.Tech shall have to undergo mandatory internship of 4 weeks during the vacation of VI and VII semesters and /or VII and VIII semesters. A University examination shall be conducted during VIII semester and the prescribed credit included in VIII semester. Internship shall be considered as a head of passing and shall be considered for the award of degree. Those, who do not take-up/complete the internship shall be declared fail and shall have to complete during subsequent University examination after satisfying the internship requirements.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2018 – 19
Outcome Based Education(OBE) and Choice Based Credit System (CBCS)
(Effective from the academic year 2018 – 19)

VII SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	PCC	18EC71	Computer Networks		3	--	--	03	40	60	100	
2	PCC	18EC72	VLSI Design		3	--	--	03	40	60	100	
3	PEC	18XX73X	Professional Elective - 2		3	--	--	03	40	60	100	
4	PEC	18XX74X	Professional Elective - 3		3	--	--	03	40	60	100	
5	OEC	18XX75X	Open Elective -B		3	--	--	03	40	60	100	
6	PCC	18ECL76	Computer Networks Lab		--	2	2	03	40	60	100	
7	PCC	18ECL77	VLSI Laboratory		--	2	2	03	40	60	100	
8	Project	18ECP78	Project Work Phase - 1		--	--	2	--	100	--	100	
9	Internship	--	Internship		(If not completed during the vacation of VI and VII semesters, it shall be carried out during the vacation of VII and VIII semesters)							
			TOTAL		15	4	6	21	380	420	800	

Note: PCC: Professional core, PEC: Professional Elective.

Professional Elective - 2

Course code under 18XX73X	Course Title
18EC731	Real Time System
18EC732	Satellite Communication
18EC733	Digital Image Processing
18EC734	Data Structures using C++
18EC735	DSP Algorithms &Architecture

Professional Electives - 3

Course code under 18XX74X	Course Title
18EC741	IOT & Wireless Sensor Networks
18EC742	Automotive Electronics
18EC743	Multimedia Communication
18EC744	Cryptography
18EC745	Machine Learning

Open Elective –B
(i) 18EC751 Communication Theory   (ii) 18EC752 Neural Networks

Students can select any one of the open electives offered by other Departments except those that are offered by the parent Department (Please refer the list of open electives under 18XX75X).

Selection of an open elective shall not be allowed if,

* The candidate has studied the same course during the previous semesters of the programme.
* The syllabus content of open elective is similar to that of the Departmental core courses or professional electives.
* A similar course, under any category, is prescribed in the higher semesters of the programme.

Registration to electives shall be documented under the guidance of Programme Coordinator/ Advisor/Mentor.

Project work:
Based on the ability/abilities of the student/s and recommendations of the mentor, a single discipline or a multidisciplinary project can be assigned to an individual student or to a group having not more than 4 students. In extraordinary cases, like the funded projects requiring students from different disciplines, the project student strength can be 5 or 6.

CIE procedure for Project Work Phase - 1:
(i) Single discipline:The CIE marks shall be awarded by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the Guide.

The CIE marks awarded for the project work phase -1, shall be based on the evaluation of the project work phase -1 Report (covering Literature survey, Problem identification, Objectives and Methodology), project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the Project report shall be the same for all the batch mates.

(ii) Interdisciplinary: Continuous Internal Evaluation shall be group wise at the college level with the participation of all guides of the college. Participation of external guide/s, if any, is desirable.

The CIE marks awarded for the project work phase -1, shall be based on the evaluation of project work phase -1 Report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates.

Internship: All the students admitted to III year of BE/B.Tech shall have to undergo mandatory internship of 4 weeks during the vacation of VI and VII semesters and /or VII and VIII semesters. A University examination shall be conducted during VIII semester and the prescribed credit shall be included in VIII semester. Internship shall be considered as a head of passing and shall be considered for the award of degree. Those, who do not take up/complete the internship shall be declared fail and shall have to complete during subsequent University examination after satisfying the internship requirements.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2018 – 19
Outcome Based Education(OBE) and Choice Based Credit System (CBCS)
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VIII SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination					Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	PCC	18EC81	Wireless and Cellular Communication		3	--	--	03	40	60	100	3
2	PEC	18XX82X	Professional Elective - 4		3	--	--	03	40	60	100	3
3	Project	18ECP83	Project Work Phase - 2		--	--	2	03	40	60	100	8
4	Seminar	18ECS84	Technical Seminar		--	--	2	03	100	--	100	1
5	Internship	18ECI85	Internship	Completed during the vacation/s of VI and VII semesters and /or VII and VIII semesters.)				03	40	60	100	3
				TOTAL	06	--	4	15	260	240	500	18

Note: PCC: Professional Core, PEC: Professional Elective.

Professional Electives - 4	
Course code under 18XX82X	Course Title
18EC821	Network Security
18EC822	Micro Electro Mechanical Systems
18EC823	Radar Engineering
18EC824	Optical Communication Networks
18EC825	Biomedical Signal Processing

Project Work

CIE procedure for Project Work Phase - 2:

(i) **Single discipline:** The CIE marks shall be awarded by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the Guide.

The CIE marks awarded for the project work phase -2, shall be based on the evaluation of project work phase -2 Report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates.

(ii) **Interdisciplinary:** Continuous Internal Evaluation shall be group wise at the college level with the participation of all guides of the college. Participation of external guide/s, if any, is desirable.

The CIE marks awarded for the project work phase -2, shall be based on the evaluation of project work phase -2 Report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates.

SEE for Project Work Phase - 2:

(i) **Single discipline:** Contribution to the project and the performance of each group member shall be assessed individually in semester end examination (SEE) conducted at the department.

(ii) **Interdisciplinary:** Contribution to the project and the performance of each group member shall be assessed individually in semester end examination (SEE) conducted separately at the departments to which the student/s belong to.

Internship: Those, who have not pursued /completed the internship shall be declared as fail and have to complete during subsequent University examination after satisfying the internship requirements.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card. Activity points of the students who have earned the prescribed AICTE activity Points shall be sent the University along with the CIE marks of 8th semester. In case of students who have not satisfied the AICTE activity Points at the end of eighth semester, the column under activity Points shall be marked NSAP (Not Satisfied Activity Points).

SCHEME OF TEACHING AND EXAMINATION
B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

III SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT31	Engineering Mathematics –III*	Maths	04		03	60	40	100	4
2	17EC32	Electronic Instrumentation	EC	03		03	60	40	100	3
3	17EC33	Analog Electronics	EC	04		03	60	40	100	4
4	17EC34	Digital Electronics	EC	04		03	60	40	100	4
5	17EC35	Network Analysis	EC	04		03	60	40	100	4
6	17EC36	Engineering Electromagnetics	EC	04		03	60	40	100	4
7	17ECL37	Analog Electronics Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL38	Digital Electronics Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
9	17KL/CPH39/49	Kannada/Constitution of India, Professional Ethics and Human Rights	Humanities	01		01	30	20	50	01
		TOTAL		**Theory: 24hours Practical: 06 hours**		**25**	**510**	**340**	**850**	**28**

1.Kannada/Constitution of India, Professional Ethics and Human Rights: 50 % of the programs of the Institution have to teach Kannada/Constitution of India, Professional Ethics and Human Rights in cycle based concept during III and IV semesters.

2. Audit Course:
(i) *All lateral entry students (except B.Sc candidates) have to register for Additional Mathematics – I, which is 03 contact hours per week.

1	17MATDIP31	Additional Mathematics –I	Maths	03		03	60	--	60	--

(ii) Language English (Audit Course) be compulsorily studied by all lateral entry students (except B.Sc candidates)

B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

IV SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT41	Engineering Mathematics –IV*	Maths	04		03	60	40	100	4
2	17EC42	Signals and Systems	EC	04		03	60	40	100	4
3	17EC43	Control Systems	EC	04		03	60	40	100	4
4	17EC44	Principles of Communication Systems	EC	04		03	60	40	100	4
5	17EC45	Linear Integrated Circuits	EC	04		03	60	40	100	4
6	17EC46	Microprocessor	EC	03		03	60	40	100	3
7	17ECL47	Microprocessor Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL48	Linear ICs and Communication Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
9	17KL/CPH39/49	Kannada/Constitution of India, Professional Ethics and Human Rights	Humanities	01		01	30	20	50	01
TOTAL				Theory: 24hours Practical: 06 hours		25	510	340	850	28

1. Kannada/Constitution of India, Professional Ethics and Human Rights: 50 % of the programs of the Institution have to teach Kannada/Constitution of India, Professional Ethics and Human Rights in cycle based concept during III and IV semesters.

2.Audit Course:
(i) *All lateral entry students (except B.Sc candidates) have to register for Additional Mathematics – II, which is 03 contact hours per week.

1	17MATDIP41	Additional Mathematics –II	Maths	03		03	60	--	60	--

(ii) Language English (Audit Course) be compulsorily studied by all lateral entry students (except B.Sc candidates)

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B.E.: Electronics & Communication Engineering

V SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17ES51	Management and Entrepreneurship Development	EC	04		03	60	40	100	4
2	17EC52	Digital Signal Processing	EC	04		03	60	40	100	4
3	17EC53	Verilog HDL	EC	04		03	60	40	100	4
4	17EC54	Information Theory & Coding	EC	04		03	60	40	100	4
5	17EC55X	Professional Elective-1	EC	03		03	60	40	100	3
6	17EC56X	Open Elective-1	EC	03		03	60	40	100	3
7	17ECL57	DSP Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL58	HDL Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
		TOTAL		**Theory: 22hours Practical: 06 hours**		24	480	320	800	26

Professional Elective-1		Open Elective – 1*** (List offered by EC/TC Board only)	
17EC551	Nanoelectronics	17EC561	Automotive Electronics
17EC552	Switching & Finite Automata Theory	17EC562	Object Oriented Programming Using C++
17EC553	Operating System	17EC563	8051 Microcontroller
17EC554	Electrical Engineering Materials		
17EC555	MSP430 Microcontroller		

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives).

Selection of an open elective is not allowed, if:

· The candidate has no pre – requisite knowledge.

· The candidate has studied similar content course during previous semesters.

· The syllabus content of the selected open elective is similar to that of Departmental core course(s) or to be studied Professional elective(s).

Registration to open electives shall be documented under the guidance of Programme Coordinator and Adviser.

B.E.: Electronics & Communication Engineering

VI SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17EC61	Digital Communication	EC	04		03	60	40	100	4
2	17EC62	ARM Microcontroller & Embedded Systems	EC	04		03	60	40	100	4
3	17EC63	VLSI Design	EC	04		03	60	40	100	4
4	17EC64	Computer Communication Networks	EC	04		03	60	40	100	4
5	17EC65X	Professional Elective-2	EC	03		03	60	40	100	3
6	17EC66X	Open Elective-2	EC	03		03	60	40	100	3
7	17ECL67	Embedded Controller Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL68	Computer Networks Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
		TOTAL		**Theory: 22hours Practical: 06 hours**		24	480	320	800	26

Professional Elective-2		Open Elective – 2*** (List offered by EC/TC Board only)	
17EC651	Cellular Mobile Communication	17EC661	Data Structures Using C++
17EC652	Adaptive Signal Processing	17EC662	Power Electronics *(not for E&C students)*
17EC653	Artificial Neural Networks	17EC663	Digital System Design using Verilog
17EC654	Digital Switching Systems		
17EC655	Microelectronics		

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives).

Selection of an open elective is not allowed, if:

· The candidate has no pre – requisite knowledge.

· The candidate has studied similar content course during previous semesters.

· The syllabus content of the selected open elective is similar to that of Departmental core course(s) or to be studied Professional elective(s).

Registration to open electives shall be documented under the guidance of Programme Coordinator and Adviser.

B.E.: Electronics & Communication Engineering

VII SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week Theory	Teaching Hours /Week Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	Credits
1	17EC71	Microwave and Antennas	EC	04		03	60	40	100	4
2	17EC72	Digital Image Processing	EC	04		03	60	40	100	4
3	17EC73	Power Electronics	EC	04		03	60	40	100	4
4	17EC74X	Professional Elective-3	EC	03		03	60	40	100	3
5	17EC75X	Professional Elective-4	EC	03		03	60	40	100	3
6	17ECL76	Advanced Communication Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
7	17ECL77	VLSI Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECP78	Project Work Phase–I + Project work Seminar	EC		03		-	100	100	2
		TOTAL		**Theory:18 hours Practical and Project: 09 hours**		**21**	**420**	**380**	**800**	**24**

Professional Elective-3		Professional Elective-4	
17EC741	Multimedia Communication	17EC751	DSP Algorithms and Architecture
17EC742	Biomedical Signal Processing	17EC752	IOT and Wireless Sensor Networks
17EC743	Real Time Systems	17EC753	Pattern Recognition
17EC744	Cryptography	17EC754	Advanced Computer Architecture
17EC745	CAD for VLSI	17EC755	Satellite Communication

1. **Project Phase – I and Project Seminar:** Comprises of Literature Survey, Problem identification, Objectives and Methodology. CIE marks shall be based on the report covering Literature Survey, Problem identification, Objectives and Methodology and Seminar presentation skill.

B.E.: Electronics & Communication Engineering

VIII SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17EC81	Wireless Cellular and LTE 4G Broadband	EC	4	-	3	60	40	100	4
2	17EC82	Fiber Optics & Networks	EC	4	-	3	60	40	100	4
3	17EC83X	Professional Elective-5	EC	3	-	3	60	40	100	3
4	17EC84	Internship/Professional Practice	EC	Industry Oriented		3	50	50	100	2
5	17ECP85	Project Work	EC	-	6	3	100	100	200	6
6	17ECS86	Seminar	EC	-	4	-	-	100	100	1
		TOTAL		**Theory: 11 hours Project and Seminar: 10 hours**		**15**	**330**	**370**	**700**	**20**

Professional Elective -5	
17EC831	Micro Electro Mechanical Systems
17EC832	Speech Processing
17EC833	Radar Engineering
17EC834	Machine learning
17EC835	Network and Cyber Security

1. Internship/ Professional Practice: 4 Weeks internship to be completed between the (VI and VII semester vacation) and/or (VII and VIII semester vacation) period.

SCHEME OF TEACHING AND EXAMINATION
B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

III SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT31	Engineering Mathematics –III*	04		03	80	20	100	4
2	15EC32	Analog Electronics	04		03	80	20	100	4
3	15EC33	Digital Electronics	04		03	80	20	100	4
4	15EC34	Network Analysis	04		03	80	20	100	4
5	15EC35	Electronic Instrumentation	04		03	80	20	100	4
6	15EC36	Engineering Electromagnetics	04		03	80	20	100	4
7	15ECL37	Analog Electronics Lab		1I+2P	03	80	20	100	2
8	15ECL38	Digital Electronics Lab		1I+2P	03	80	20	100	2
		TOTAL	**24**	**6**	**24**	**640**	**160**	**800**	**28**

*Additional course for Lateral entry students only:

1	15MATDIP31	Additional Mathematics - I	03		03	80	--	80	--

1

SCHEME OF TEACHING AND EXAMINATION
B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

IV SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical / Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT41	Engineering Mathematics –IV*	04		03	80	20	100	4
2	15EC42	Microprocessor	04		03	80	20	100	4
3	15EC43	Control Systems	04		03	80	20	100	4
4	15EC44	Signals and Systems	04		03	80	20	100	4
5	15EC45	Principles of Communication Systems	04		03	80	20	100	4
6	15EC46	Linear Integrated Circuits	04		03	80	20	100	4
7	15ECL47	Microprocessor Lab		1I+2P	03	80	20	100	2
8	15ECL48	Linear ICs and Communication Lab		1I+2P	03	80	20	100	2
		TOTAL	**24**	**06**	**24**	**640**	**160**	**800**	**28**

*Additional course for Lateral entry students only:

| 1 | 15MATDIP41 | Additional Mathematics - II | 03 | | 03 | 80 | -- | 80 | -- |

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

V SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical /Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15ES51	Management and Entrepreneurship Development	04		03	80	20	100	4
2	15EC52	Digital Signal Processing	04		03	80	20	100	4
3	15EC53	Verilog HDL	04		03	80	20	100	4
4	15EC54	Information Theory & Coding	04		03	80	20	100	4
5	15EC55X	Professional Elective-1	03		03	80	20	100	3
6	15EC56X	Open Elective-1	03		03	80	20	100	3
7	15ECL57	DSP Lab		1I+2P	03	80	20	100	2
8	15ECL58	HDL Lab		1I+2P	03	80	20	100	2
TOTAL			**22**	**06**	**24**	**640**	**160**	**800**	**26**

Professional Elective-1		Open Elective – 1* (List offered by EC/TC Board only)	
15EC551	Nanoelectronics	15EC561	Automotive Electronics
15EC552	Switching & Finite Automata Theory	15EC562	Object Oriented Programming Using C++
15EC553	Operating System	15EC563	8051 Microcontroller
15EC554	Electrical Engineering Materials		
15EC555	MSP430 Microcontroller		

1. Professional Elective: Elective relevant to chosen specialization/ branch.
2. * Open Elective List: For other Open Electives offered by other Boards, refer the Scheme of other Boards or Consolidated list in VTU Website.

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

VI SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15EC61	Digital Communication	04		03	80	20	100	4
2	15EC62	ARM Microcontroller & Embedded Systems	04		03	80	20	100	4
3	15EC63	VLSI Design	04		03	80	20	100	4
4	15EC64	Computer Communication Networks	04		03	80	20	100	4
5	15EC65X	Professional Elective-2	03		03	80	20	100	3
6	15EC66X	Open Elective-2	03		03	80	20	100	3
7	15ECL67	Embedded Controller Lab		1I+2P	03	80	20	100	2
8	15ECL68	Computer Networks Lab		1I+2P	03	80	20	100	2
		TOTAL	**22**	**6**	**24**	**640**	**160**	**800**	**26**

Professional Elective-2		Open Elective – 2* (List offered by EC/TC Board only)	
15EC651	Cellular Mobile Communication	15EC661	Data Structures Using C++
15EC652	Adaptive Signal Processing	15EC662	Power Electronics
15EC653	Artificial Neural Networks	15EC663	Digital System Design using Verilog
15EC654	Digital Switching Systems		
15EC655	Microelectronics		

1. Professional Elective: Elective relevant to chosen specialization/branch.
2. * Open Elective List: For other Open Electives offered by other Boards, refer the Scheme of other Boards or Consolidated list in VTU Website.

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

| Sl. No | Subject Code | Title | Teaching Hours /Week | | Examination | | | | 15EC |
			Theory	Practical/Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15EC71	Microwave and Antennas	04		03	20	80	100	4
2	15EC72	Digital Image Processing	04		03	20	80	100	4
3	15EC73	Power Electronics	04		03	20	80	100	4
4	15XX74X	Professional Elective-3	03		03	20	80	100	3
5	15EC75X	Professional Elective-4	03		03	20	80	100	3
6	15ECL76	Advanced Communication Lab		1I+2P	03	20	80	100	2
7	15ECL77	VLSI Lab		1I+2P	03	20	80	100	2
8	15ECP78	Project Work Phase–I + Project work Seminar		03		100	-	100	2
		TOTAL	**18**	**09**	**21**	**240**	**560**	**800**	**24**

Professional Elective-3		Professional Elective-4	
15EC741	Multimedia Communication	15EC751	DSP Algorithms and Architecture
15EC742	Biomedical Signal Processing	15EC752	IoT and Wireless Sensor Networks
15EC743	Real Time Systems	15EC753	Pattern Recognition
15EC744	Cryptography	15EC754	Advanced Computer Architecture
15EC745	CAD for VLSI	15EC755	Satellite Communication

1. Project Phase –I + Project Work Seminar: Literature Survey, Problem Identification, Objectives and Methodology. Submission of Synopsis and Seminar.

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

VIII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15EC81	Wireless Cellular and LTE 4G Broadband	4	-	3	20	80	100	4
2	15EC82	Fiber Optics & Networks	4	-	3	20	80	100	4
3	15EC83X	Professional Elective-5	3	-	3	20	80	100	3
4	15EC84	Internship/Professional Practice	Industry Oriented		3	50	50	100	2
5	15ECP85	Project Work	-	6	3	100	100	200	6
6	15ECS86	Seminar	-	4	-	100	-	100	1
		TOTAL	**11**	**10**	**15**	**310**	**390**	**700**	**20**

Professional Elective -5	
15EC831	Micro Electro Mechanical Systems
15EC832	Speech Processing
15EC833	Radar Engineering
15EC834	Machine learning
15EC835	Network and Cyber Security

1. Internship / Professional Practice: To be carried between the (6th and 7th Semester) or (7th and 8th) Semester Vacation period.

SAIRAM
COLLEGE OF ENGINEERING

	Title Lesson Plan		
Format No. FR-019	Rev. No. 01	Issue Date: 15/04/2016

Academic Year : 2020-2021

Class : ECE / II - Year /IV SEM / A

Staff Name : B.Srilatha

Course Name : 18EC45 - Signals and Systems

Hours / Week : 4
Total Hours : 40
IA Mark : 40
Exam Hours : 3
Exam Marks : 100
Topic Type : Module

S.No	Proposed Date	Period	Module	Title of the Module	Planned Topics	COC	BTL	ICT/Activity	Text Book / References
1	19/04/2021	4	Module-1	Module 1:Introduction and Classification of signals	1.Definition of signals and sytems,communication and control system as examples Classification of signals.	CO1	L1	PPT	Text:1 Simon Haykin and Barry Van Veen." Signals and Systems"Text1:1.1-1.4
2	22/04/2021	3	Module-1	Module 1:Basic Operations on signals:	2.Amplitude scaling, addition, multiplication	CO1	L1,L2	PPT	Text1:1.5
3	23/04/2021	1	Module-1	Module 1:Basic Operations on signals:	3.differentiation,integration, time scaling,	CO1	L2,L3	PPT	Text1:1.4
4	26/04/2021	4	Module-1	Module 1:Basic Operations on signals:	4.time shift and time reversal	CO1	L2,L3	PPT	Text1:1.5
5	29/04/2021	3	Module-1	Module 1: Elementary signals/Functions	5. Exponential, sinusoidal, step	CO1	L2,L3	PPT	Text1:1.6
6	30/04/2021	1	Module-1	Module 1: Elementary signals/Functions	6.impulse and ramp functions.	CO1	L2,L3	PPT	Text1:1.6

S.No	Proposed Date	Period	Module	Title of the Module	Planned Topics	COC	BTL	ICT/Activity	Text Book / References
7	03/05/2021	4	Module-1	Module 1: Elementary signals/Functions	7.Expression of triangular, rectangular	CO1	L2,L3	PPT	Text1:1.6
8	06/05/2021	3	Module-1	Module 1: Elementary signals/Functions	8.other waveforms in terms of elementary signals	CO1	L2,L3	PPT/Assignment	Text1:1.6
9	07/05/2021	1	Module-2	Module 2: System Classification and properties:	9. Linear-nonlinear, Time variant-invariant,	CO2,CO3	L1,L2. L3	PPT	Text1:1.8
10	10/05/2021	4	Module-2	Module 2: System Classification and properties:	10.causal-noncausal, static-dynamic, stable-unstable, invertible.	CO2,CO3	L1,L2. L3	PPT	Text1:1.8
11	13/05/2021	3	Module-2	Module 2:Time domain representation of LTI System:	11.Impulse response, convolution sum, convolution integral.	CO2,CO3	L2,L3	PPT	Text 1:2.1,2.2,2.3
12	14/05/2021	1	Module-2	Module 2:Time domain representation of LTI System:	12.Computation of convolution sum using graphical method for unit step and unit step,unit step and exponential	CO2,CO3	L2,L3	PPT	Text 1:2.3
13	17/05/2021	4	Module-2	Module 2:Time domain representation of LTI System:	13Convolution sum- Unit step to rectangular, rectangular to rectangular	CO2,CO3	L2,L3	PPT	Text 1:2.3

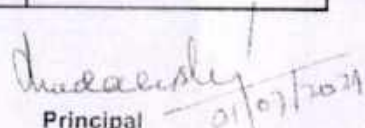| S.No	Proposed Date	Period	Module	Title of the Module	Planned Topics	COC	BTL	ICT/Activity	Text Book / References
14	20/05/2021	3	Module-2	Module 2:Time domain representation of LTI System:	14.Computation of convolution integral using graphical method for unit step and unit step,unit step and exponential	CO2,CO3	L2,L3	PPT	Text 1:2.4
15	21/05/2021	1	Module-2	Module 2:Time domain representation of LTI System:	15.Convolution Integral- Unit step to rectangular,Exponential to exponential	CO2,CO3	L2,L3	PPT	Text 1:2.4
16	27/05/2021	3	Module-2	Module 2:Time domain representation of LTI System:	16.Problems on convolution integral	CO2,CO3	L3	PPT/Assignment	Text 1:2.5
17	28/05/2021	1	Module-3	Module 3: LTI system Properties in terms of impulse response:	17.Properties of convolution,System interconnection, Memory less	CO4	L1,L2.L3	PPT	Text 1:2.6
18	31/05/2021	4	Module-3	Module 3: LTI system Properties in terms of impulse response:	18.Properties of convolution Causal	CO4	L2,L3	PPT	Text 1:2.7
19	03/06/2021	3	Module-3	Module 3: LTI system Properties in terms of impulse response:	19.Properties of convolution Stable,	CO4	L2,L3	PPT	Text 1:2.7
20	04/06/2021	1	Module-3	Module 3: LTI system Properties in terms of impulse response:	20.Properties of convolution Invertible	CO4	L2,L3	PPT	Text 1:2.7
21	07/06/2021	4	Module-3	Module 3: LTI system Properties in terms of impulse response:	21.Properties of convolution Deconvolution,	CO4	L2,L3	PPT	Text1:2.7

S.No	Proposed Date	Period	Module	Title of the Module	Planned Topics	COC	BTL	ICT/Activity	Text Book / References
22	10/06/2021	3	Module-3	Module 3: LTI system Properties in terms of impulse response:	22.Properties of convolution step response	CO4	L2,L3	PPT	Text1:2.8
23	11/06/2021	1	Module-3	Module 3:Fourier Representation of Periodic Signals:	23.Properties of CTFS	CO4	L2,L3	PPT	Text1:3.3
24	14/06/2021	4	Module-3	Module 3:Fourier Representation of Periodic Signals:	24.Problems on CTFS	CO4	L3	PPT/Assignm ent	Text1:3.3
25	17/06/2021	3	Module-4	Module 4: Fourier Representation of aperiodic Signals:	25.Introduction to DTFS , definition	CO4	L1	PPT	Text1:3.4
26	18/06/2021	1	Module-4	Module 4: Fourier Representation of aperiodic Signals:	26.Problems on DTFS	CO4	L3	PPT	Text1:3.4
27	21/06/2021	4	Module-4	Module 4: Properties of Fourier Transform:	27.Linearity, Time shift,	CO4	L2,L3	PPT	Text1:3.8,?.12
28	24/06/2021	3	Module-4	Module 4: Properties of Fourier Transform:	28.Frequency shift, Scaling	CO4	L2,L3	PPT	Text1:3.12,3.15
29	25/06/2021	1	Module-4	Module 4: Properties of Fourier Transform:	29.Convolution and Modulation,	CO4	L2,L3	PPT	Text1:3.10

S.No	Proposed Date	Period	Module	Title of Module	Planned Topics	CO	BTL	ICT/Activity	Text Book / References
30	01/07/2021	3	Module-4	Module 4: Properties of Fourier Transform:	30.Parseval's theorem	CO4	L2,L3	PPT	Text1:3.16
31	02/07/2021	1	Module-4	Module 4: Properties of Fourier Transform:	31.problems on properties of Fourier Transform.	CO4	L3	PPT	Text1:3.3-3.16
32	05/07/2021	4	Module-4	Module 4: Properties of Fourier Transform:	32.problems on properties of Fourier Transform.	CO4	L3	PPT/Assignm ent	Text1:3.3-3.16
33	08/07/2021	3	Module-5	Module 5:The Z-Transforms:	33.Z transform, properties of the region of convergence,	CO5	L1,L2	PPT	Text1:7.1,7.2,7.3
34	09/07/2021	1	Module-5	Module 5:The Z-Transforms:	34.Properties of Z-transform	CO5	L2,L3	PPT	Text1:7.4
35	12/07/2021	4	Module-5	Module 5:The Z-Transforms:	35.Problems on properties of Z transform	CO5	L3	PPT	Text1:7.4
36	15/07/2021	3	Module-5	Module 5:The Z-Transforms:	36.Problems on properties of Z transform	CO5	L3	PPT	Text1:7.4
37	16/07/2021	1	Module-5	Module 5:The Z-Transforms:	37.Inverse Z transform,Causality and stability,	CO5	L2,L3	PPT	Text1:7.5
38	19/07/2021	4	Module-5	Module 5:The Z-Transforms:	38.Problems on inverse Z transform	CO5	L3	PPT	Text1:7.5

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S.No	Proposed Date	Period	Module	Title of the Module	Planned Topics	COC	BTL	ICT/Activity	Text Book / References
39	22/07/2021	3	Module-5	Module 5:The Z-Transforms:	39.Transform analysis of LTI systems	CO5	L2,L3	PPT	Text1:7.6
40	23/07/2021	1	Module-5	Module 5:The Z-Transforms:	40.Transform analysis of LTI systems-problems	CO5	L2,L3	PPT/Assignment	Text1:7.6

Faculty In-charge

Head of the Department

Principal 01/07/2021

SAIRAM COLLEGE OF ENGINEERING

Branch : ECE

Year : II - Year

Academic Year : 2020-2021

Sem / Sec : IV SEM / A

S.No	Subject Code	Subject Title	Hours/ Week	Total Hours Required to Complete Subject	Total Work Load	Section	Faculty Allocated	Resourse Required	University Guidelines
1	18ECL47	Microcontroller Lab	4	42	4	A	Aruna R,Halesha H R	CKS,Hands on Training	4
2	18ECL48	Analog Electronics Lab	4	42	4	A	A.Poonguzhali,Santosh Kumar N	CKS,Hands on Training	4
3	18KAK49	Aadalitha Kannada	2	25	4	A	P.Venugopal	Black Board,LCD	2
4	18KVK49	Vyavaharika Kannada	2	25	4	A	Aruna R	Black Board,LCD	2
5	18MAT41	Complex analysis, probability and statistical methods	5	50	10	A	B Jyothi	Black Board,LCD	4
6	18EC42	Analog Circuits	5	50	10	A	Santosh Kumar N	Black Board,LCD	5
7	18EC43	Control Systems	4	40	8	A	Savitha H.S	Black Board,LCD	3
8	18EC44	Engineering Statistics and Linear Algebra	4	40	8	A	Venkatesha P	Black Board,LCD	3
9	18EC45	Signals and Systems	4	40	8	A	B.Srilatha	Black Board,LCD	3
10	18EC46	Microcontroller	4	40	8	A	Halesha H R	Black Board,LCD	3

HOD

17/09/2021

SAIRAM
COLLEGE OF ENGINEERING

Faculty Name : Halesha H R / ECE

S.NO	USN	STUDENT NAME	CLASS	FEE STATUS	SIGNATURE
1	1SB17EC034	PRADYUMANSHARMA	ECE II - Year III SEM A	Partial Paid	
2	1SB18EC035	PRABUDH SAGARV	ECE II - Year III SEM A	Partial Paid	
3	1SB18EC021	KARTHIKV	ECE II - Year III SEM A	Partial Paid	
4	1SB18EC002	AKSHAY N	ECE III - Year V SEM A	Full Paid	
5	1SB18EC008	B NAVEEN KUMAR REDDY	ECE III - Year V SEM A	Partial Paid	
6	1SB18EC009	BELLAMKONDA MadhaviB R	ECE III - Year V SEM A	Partial Paid	
7	1SB18EC015	DIVYA M	ECE III - Year V SEM A	Partial Paid	
8	1SB18EC017	HARSHITHAM	ECE III - Year V SEM A	Partial Paid	
9	1SB18EC023	KAVYAE	ECE III - Year V SEM A	Permission	
10	1SB18EC025	LITHINKUMAR R	ECE III - Year V SEM A	Partial Paid	
11	1SB18EC026	MANJUNATH K B	ECE III - Year V SEM A	Full Paid	
12	1SB18EC033	PAVITHRAK	ECE III - Year V SEM A	Partial Paid	
13	1SB18EC037	PRIYANKAN	ECE III - Year V SEM A	Partial Paid	
14	1SB18EC040	ROHITH VIGNESHG	ECE III - Year V SEM A	Partial Paid	
15	1SB18EC042	SHRUTHI KULKARNI	ECE III - Year V SEM A	Full Paid	
16	1SB18EC045	SUMANTHG	ECE III - Year V SEM A	Partial Paid	
17	1SB18EC046	VASANTH U	ECE III - Year V SEM A	Partial Paid	
18	1SB19EC400	PRAMOD PRASAD G	ECE III - Year V SEM A	Partial Paid	
19	1SB18EC007	B MAHASVIN	ECE III - Year V SEM A	Partial Paid	

Mentor Signature

Head of the Department

SAIRAM
COLLEGE OF ENGINEERING
Anekal, Bengaluru

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Lecture Videos Link

CSE Department

https://www.youtube.com/channel/UC75OJIQS9kECngmkuhMmTSQ

ECE Department

https://www.youtube.com/channel/UCZsBCZcFk-deSy_6lpUW8YA

EEE Department

https://www.youtube.com/channel/UC8M6KEHN3HRb2AqgbuXnAiQ

MECH Department

https://www.youtube.com/channel/UCbbtoYt3p-R9L4nwvaN2fGA

Lecture Notes Link

http://sairamce.edu.in/library/