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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

DATE: 13.11.2021

<u>Report</u>

<u>ON</u>

GUEST LECTURE - ON "VLSI DSP ARCHITECTURES"

The Department of Electronics and Communication Engineering, Sri Sairam College of Engineering Organized Guest lecture on "<u>VLSI DSP ARCHITECTURES</u>" from 12th November 2021 @ 11.15am to 12.45p.m through online Zoom Platform.

The guest lecture was inaugurated by the welcome speech by **Dr.T.N. Prabakar**, Head of the department ECE also Sir introduced the speaker of the day.

Our beloved **Dr.B. Shadaksharappa**, Principal Sri Sairam College of Engineering conveyed the best wishes for organizing such guest lectures.

The resource person Dr.G. Seetharaman, Head of Department of ECE, IIIT, Tirchy. Sir has given insight about VLSI Design, design of Braun Multiplier and insight of FPGA architecture and how it used in design of digital systems and detailed talk about the various opportunities in emerging field of VLSI. Very interesting delivery. Around 60+participants joined the session. The important take away of the session was the students learnt about the practical aspects about VLSI design and DSP Architectures.

The vote of thanks to the speaker was given by **Prof. Aruna R**, Assistant Professor, ECE, Sri Sairam college of Engineering.

Coordinator (Prof.Santosh Kumar N)

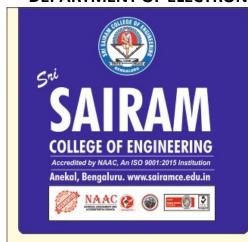
HOD (Dr.T.N. Prabakar)

PRINCIPAL Dr.B.SHADAKSHRAPPA



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Department of

ELECTRONICS & COMMUNICATION ENGG.

organizes





Speaker

Dr. G. Seetharaman Head of Department, Associate Professor

Prof. Santosh Kumar N

Dr. T N Prabakar

Dr. B. Shadaksharappa

Sri Sai Prakash LeoMuthu

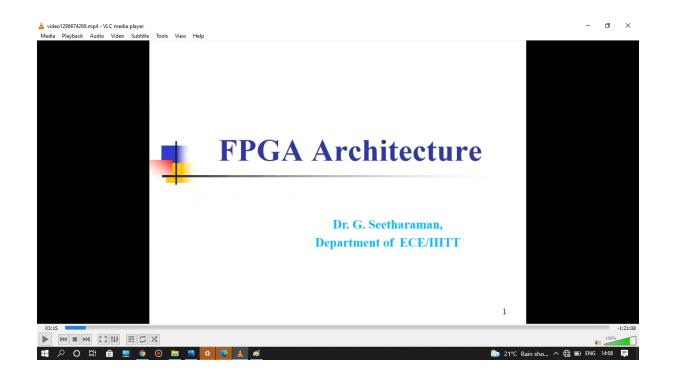
Chairman & CEO, Sairam Institutions

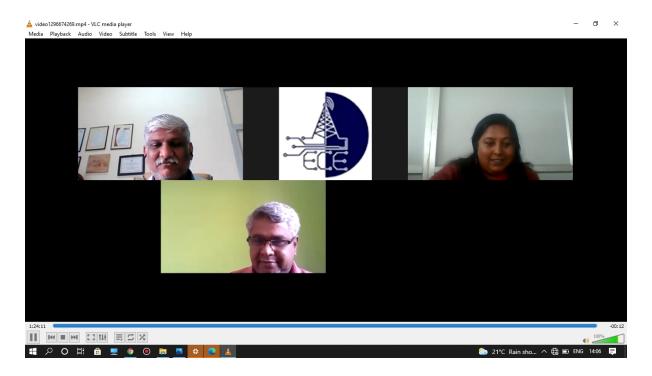






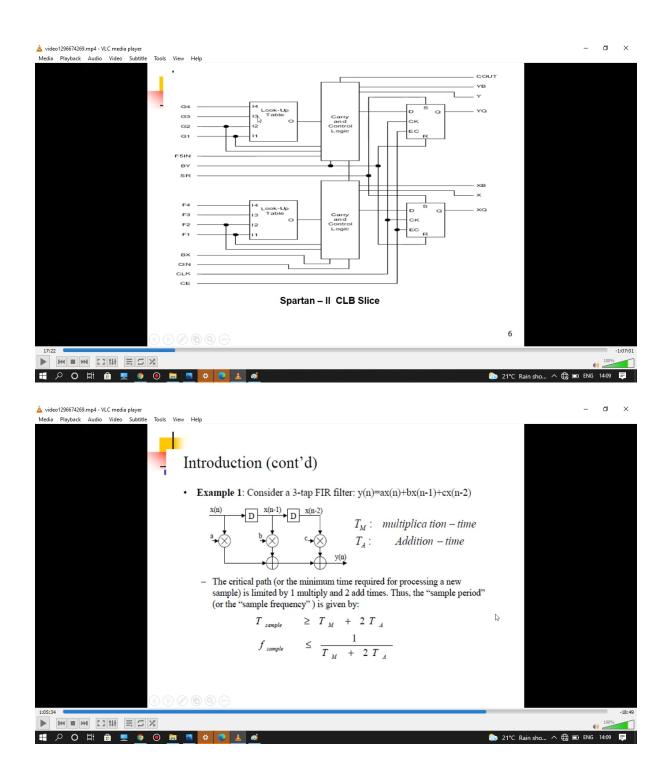
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