



Sri
SAIRAM
COLLEGE OF ENGINEERING
Anekal, Bengaluru

Accredited by NAAC
ISO 9001:2015 Certified Institution
Approved by AICTE, New Delhi
Affiliated to Visvesvaraya Technological University
www.sairamce.edu.in



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 28/02/2024

REPORT – WORKSHOP ON “EMBEDDED PCB”

Department of Electronics and Communication Engineering, Sri Sairam College of Engineering, Bangalore has conducted a two-day workshop on “Embedded PCB” during 26/02/2024 to 27/02/2024 for third year ECE students. Dr. Rajesh T, Technical Lead, Innovative Engineering Products, Hosur has been invited as Chief Guest for this workshop.

The Workshop has been inaugurated on 26/02/2024 with the presence of Dr. B. Shadaksharappa, Principal, Sri Sairam College of Engineering, Bangalore, Dr. A. Poonguzhali, Head of the Department, Department of Electronics and Communication Engineering, Sri Sairam College of Engineering, Bangalore, Heads of various Departments, Faculty members and students in AV Hall. The gathering has been welcomed by Dr. A. Poonguzhali, Head of the Department of ECE and felicitated by Dr. B. Shadaksharappa, Principal followed by the speech of the resource person Dr. Rajesh T.

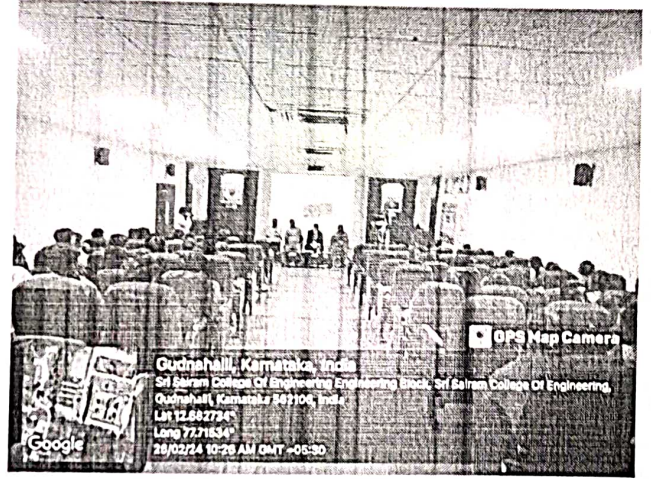
On first day forenoon, oral session has been conducted on understanding of PCB, Different types, Component Identification and Design steps in IoT Laboratory, Department of ECE. During afternoon session Software hands-on session has been given based on the topics understanding the schematic entry, Creating library and components, Preparing schematic, Introduction to PCB layout, Footprints library and browsing, Starting PCB design, Component placement, Setting up rules, Routing and Editing, DRC check, Final output generation and Bill of materials.

On second day, Hands-on session has been given on the topics like Session Sheet cutting, Film exposing and Image impression, Etching, Drilling and Tinning, Soldering, Testing and Troubleshooting. The students have given very good feedback on using Easy EDA tool and satisfied with the hands-on experience in deploying their own circuit on PCB. Students requested to conduct more events on such core topics and interested to attend internship with the invited resource person. The session was very interactive and provided a very good learning platform. Finally, the valedictory has been conducted on 27/02/2024 evening session and certificates has been distributed to the respective students.



Sri
SAIRAM
COLLEGE OF ENGINEERING
Anekal, Bengaluru

Accredited by NAAC
ISO 9001:2015 Certified Institution
Approved by AICTE, New Delhi
Affiliated to Visvesvaraya Technological University
www.sairamce.edu.in



V. [Signature] / A. [Signature]
Coordinators

A. [Signature]
28/2/24
HOD

[Signature]

[Signature]
06/03/2024